

**NSKAA**

***Bradford 10/10G***

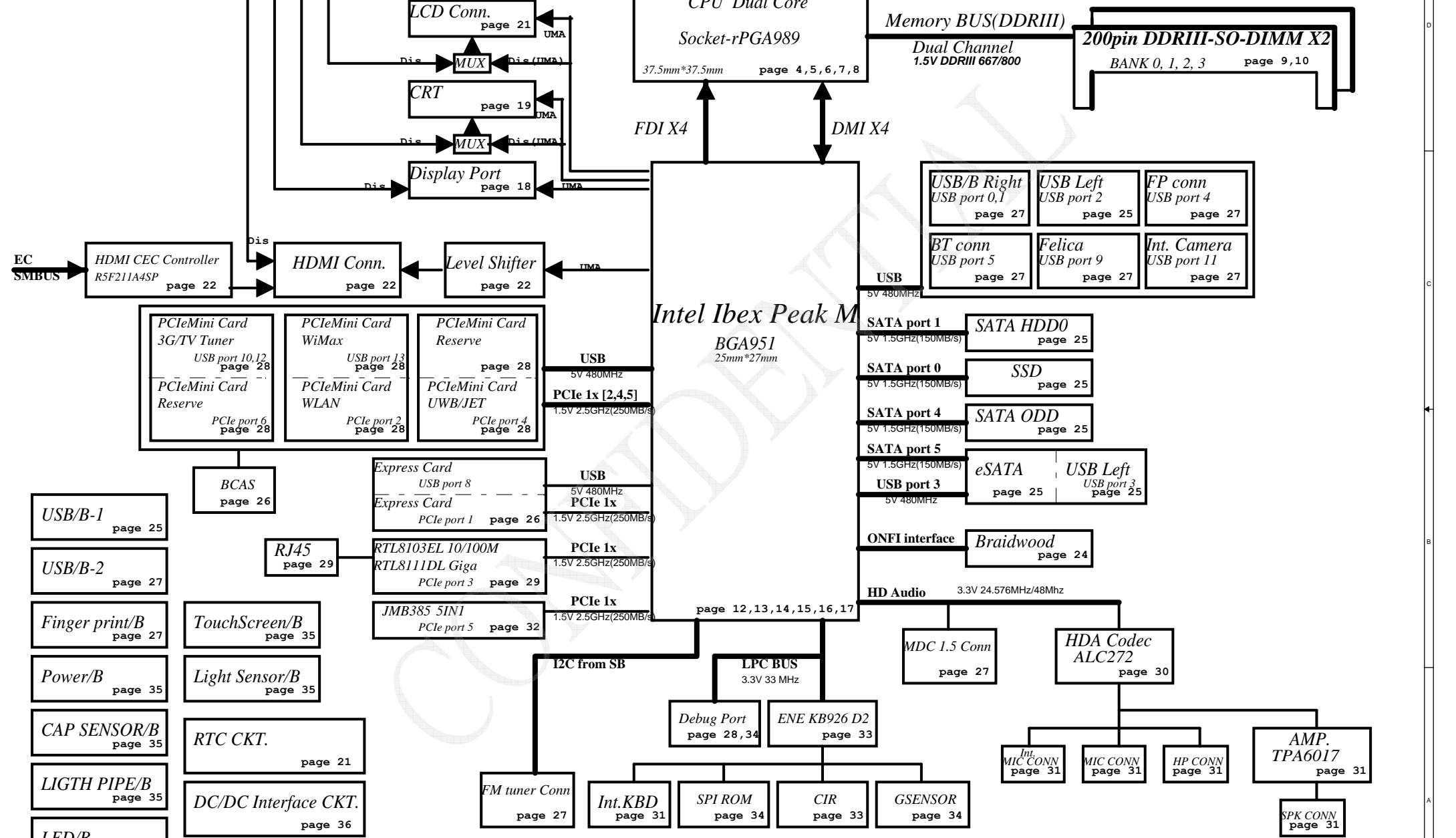
**LA-5361P REV 1.0 Schematic**

**Intel Auburndale/IBEX**

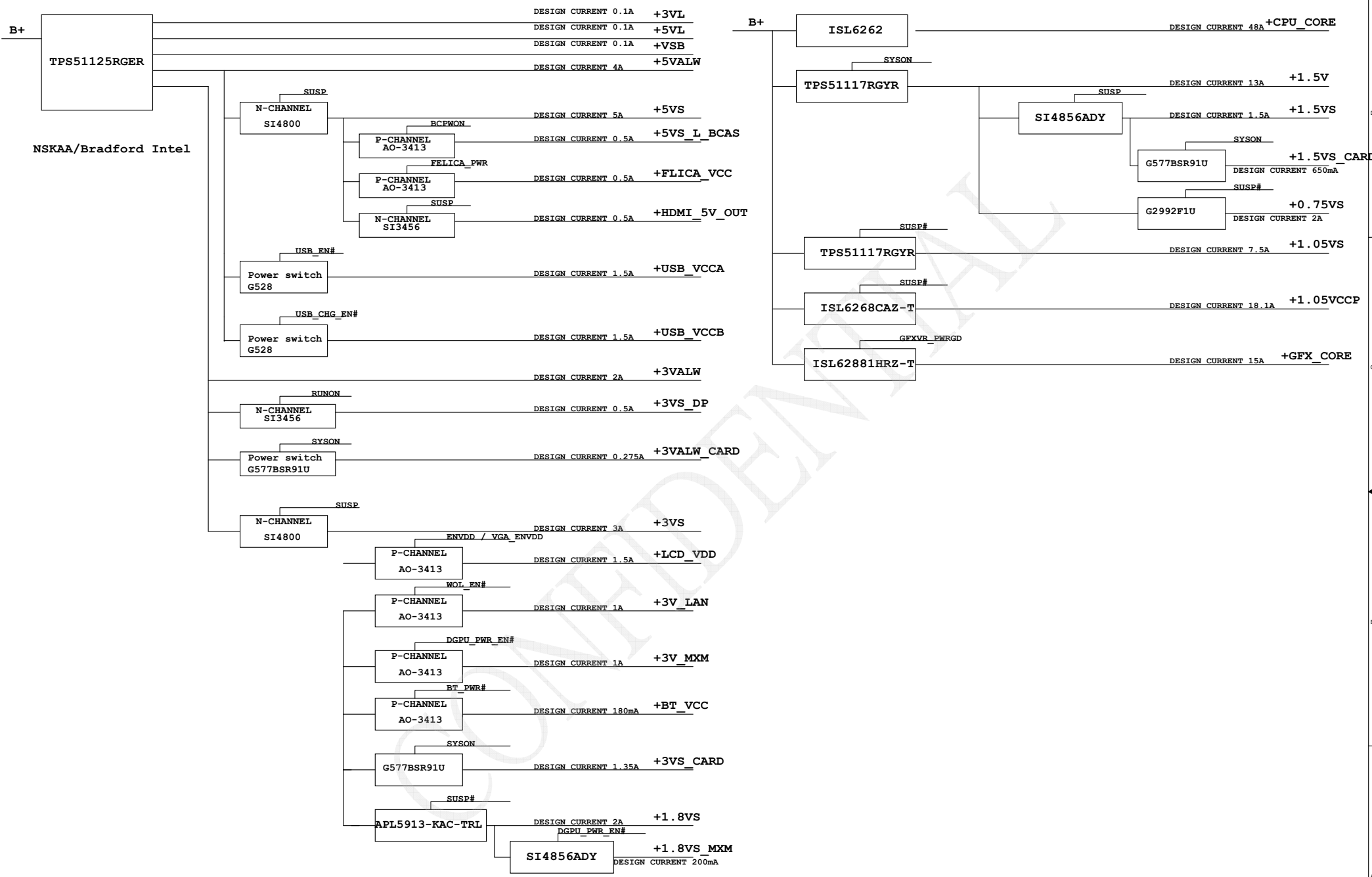
**2009-11-26 Rev. 01.0**

Security Classification		Compal Secret Data		Compal Electronics, Inc.	
Issued Date	2009/11/26	Deciphered Date	2010/11/26	Title	SCHEMATIC,MB A5361
THIS SHEET OF ENGINEERING DRAWING IS THE PROPRIETARY PROPERTY OF COMPAL ELECTRONICS, INC. AND CONTAINS CONFIDENTIAL AND TRADE SECRET INFORMATION. THIS SHEET MAY NOT BE TRANSFERRED FROM THE CUSTODY OF THE COMPETENT DIVISION OF R&D DEPARTMENT EXCEPT AS AUTHORIZED BY COMPAL ELECTRONICS, INC. NEITHER THIS SHEET NOR THE INFORMATION IT CONTAINS MAY BE USED BY OR DISCLOSED TO ANY THIRD PARTY WITHOUT PRIOR WRITTEN CONSENT OF COMPAL ELECTRONICS, INC.				Document Number	Rev
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*Fan Control*  
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<b>Security Classification</b>		<b>Compal Secret Data</b>		<b><i>Compal Electronics, Inc.</i></b>		
<b>Issued Date</b>	2009/11/26	<b>Deciphered Date</b>	2010/11/26	<b>Title</b>	SCHEMATIC, MB A5361	
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					401715	B
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( 0 MEANS ON   X MEANS OFF ) **BTO Option Table**

State \ power plane	+B +3VL +5VL	+B +3VL +5VL	+5VALN +3VALN	+1.5V	+5VS +3VS +CFCORE +VCCP +1.05VS +1.8VS +1.5VS +0.75VS +GFX_CORE
S0	O	O	O	O	O
S1	O	O	O	O	O
S3	O	O	O	O	X
S5 S4/AC	O	O	O	X	X
S5 S4/ Battery only	O	O	X	X	X
S5 S4/AC & Battery don't exist	O	X	X	X	X

SMBUS Control Table

	SOURCE	BATT	XDP	BOD18M/ LDO-55M	CLK CHIP	MINI CARD	NewCard	THERMO KB926	THERMAL SENSOR K001	G-SENSOR	Light Sensor	HDMI- CEC
SM8C18-CLK SM8C-CLK-CA1	KB926	<b>V</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>V</b>
SM8C18- SM8DATA	PCH	<b>X</b>	<b>X</b>	<b>V</b>	<b>V</b>	<b>V</b>	<b>V</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>
SM8C18-CLK SM8DATA	PCH	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>		<b>X</b>	<b>X</b>
SM8C18-CLK SM8DATA	PCH	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>V</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>
SM8C-CLK-CA2 SM8C-CLK-CA2	KB926	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>X</b>	<b>V</b>	<b>V</b>	<b>V</b>	<b>X</b>

## EC SM Bus1 address

Power	Device	Address	Power	Device	Address
+3V_L	EC x8926 D3		+3V_S	EC x8926 D3	
+3V_L	Smart Battery	0001 011x b	+3V_S	PCB	
+3V_L	HDMI-CEC	0011 010x b	+3V_S	MXM	0100 1101 b
+3V_L	CEC	0011 010x b	+3V_S	G-Sensor	00x1 1111 b
			+3V_S	Light Sensor	

## EC SM Bus2 address

Power	Device	Address
+3V <sub>S</sub>	EC K8926 D3	
+3V <sub>S</sub>	PCH	
+3V <sub>S</sub>	MMX	0100 1101 b
+3V <sub>S</sub>	G-Sensor	00x1 1111 b
+3V <sub>S</sub>	Light Sensor	

## PCH SM Bus address

Power	Device	Address
+3VALW	PCR	
+3VS	Clock Generator (SLG8SP587VTR)	
+3VS	DDR DIMM0	1001 000Xb
+3VS	DDR DIMM1	1001 010Xb
+3VS	NewCard	
+3VS	WLAN	

### BTO Option Table

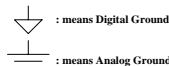
Function	HDMI			CRT	Display	LAN	
description	(Y)			(Q)	(Z)	(E)	(C)
explain	Intel (GMA)	ATI MOB/S	COMMON			10/100 Gb/s	
BTO	IHM18	NIHM18	HDM18	H8	CRT8	DP8	8103EL8 8111DL8

Function	3G SIM slot	Mini card	Felica	Finger printer	CIR	CAMERA & MIC	BLUE TOOTH
description	(3)	(D2)	(J)	(F)	(I)	(X)	(B)
explain		Two Cards				CAMERA MIC	
BTO	3G8	3G8	FLICAS	FP8	CIR8	CAME MIC8	BT8

*Stencil Stuff MEMO ver. 0.1*

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System Jumper->J3, J1, J2, J3, J4, J5, J6, J7, J8, J10, J11, J13, J20
PM->~P136,P137
GM:SW->~P19,J12
Stand-Off:
H26,H27,H45,H44(BOT side)
JMXM1->H20,H21(BOT side)
JMDC->H22, H23(BOT side)
JNAND->H38,H42(BOT side)
JGPS->H36,H39(BOT side)
JWLAN->H40,H41(TOP side)
Power Jumper->
```

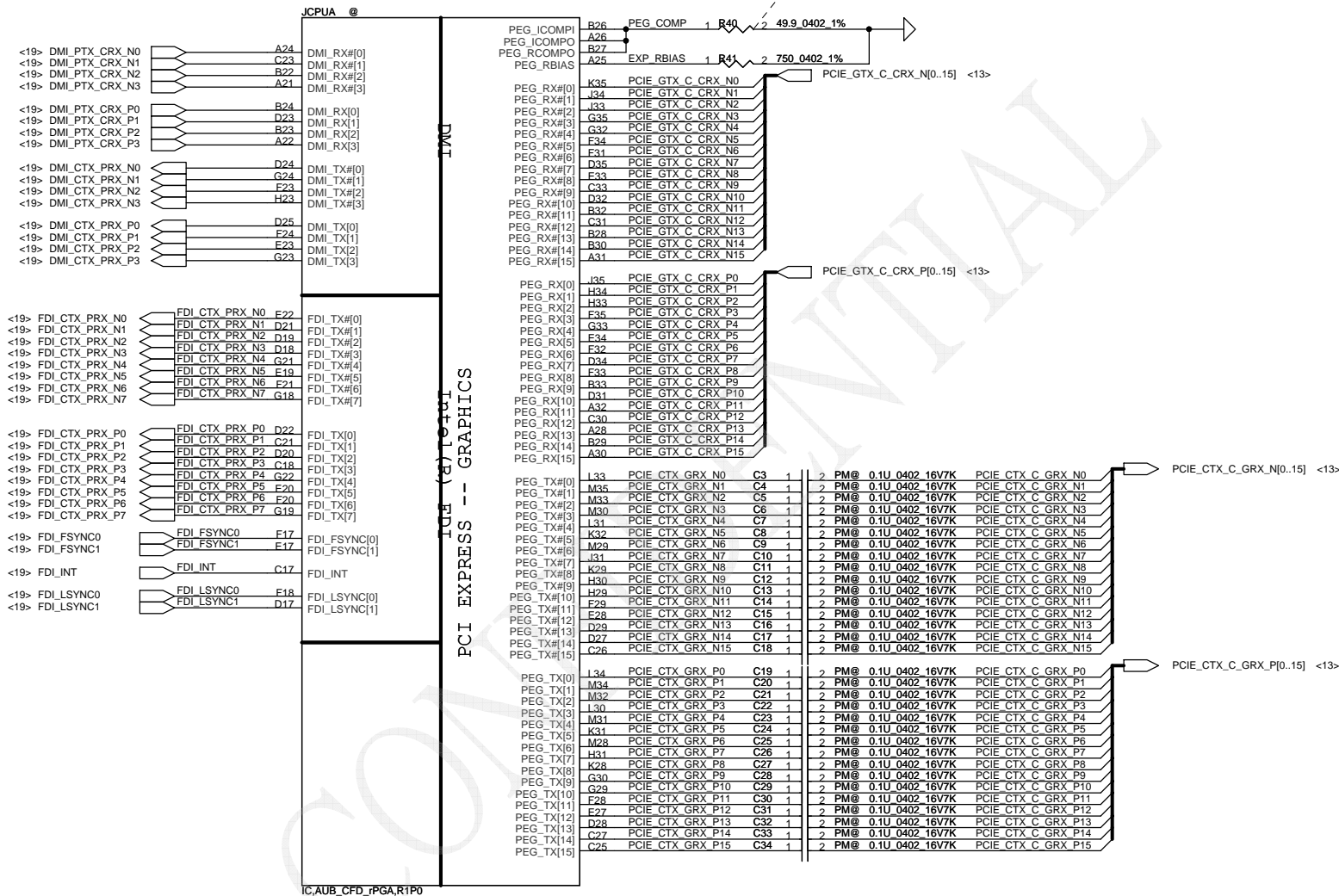
**Symbol Note :**



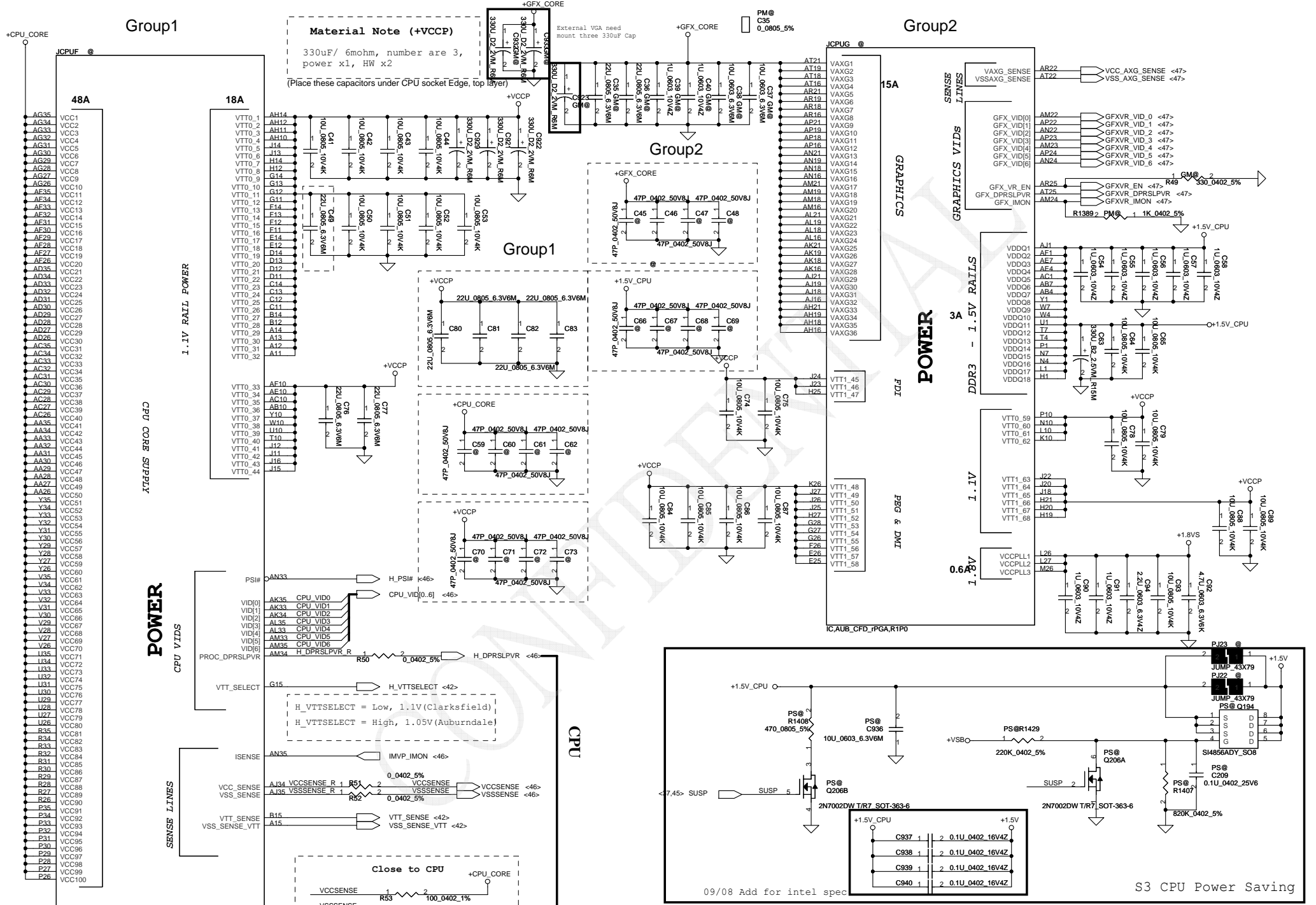
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Security Classification	Compel Secret Data		File	
Issued Date	2009/11/26	Deciphered Date	2010/1/26	
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Document Number	401715		Rev	



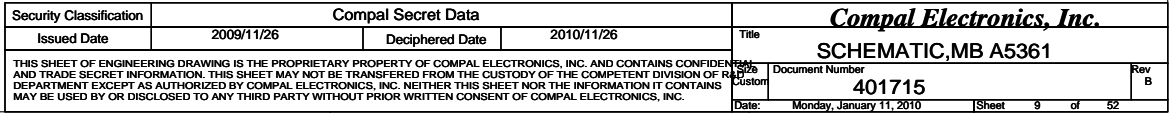


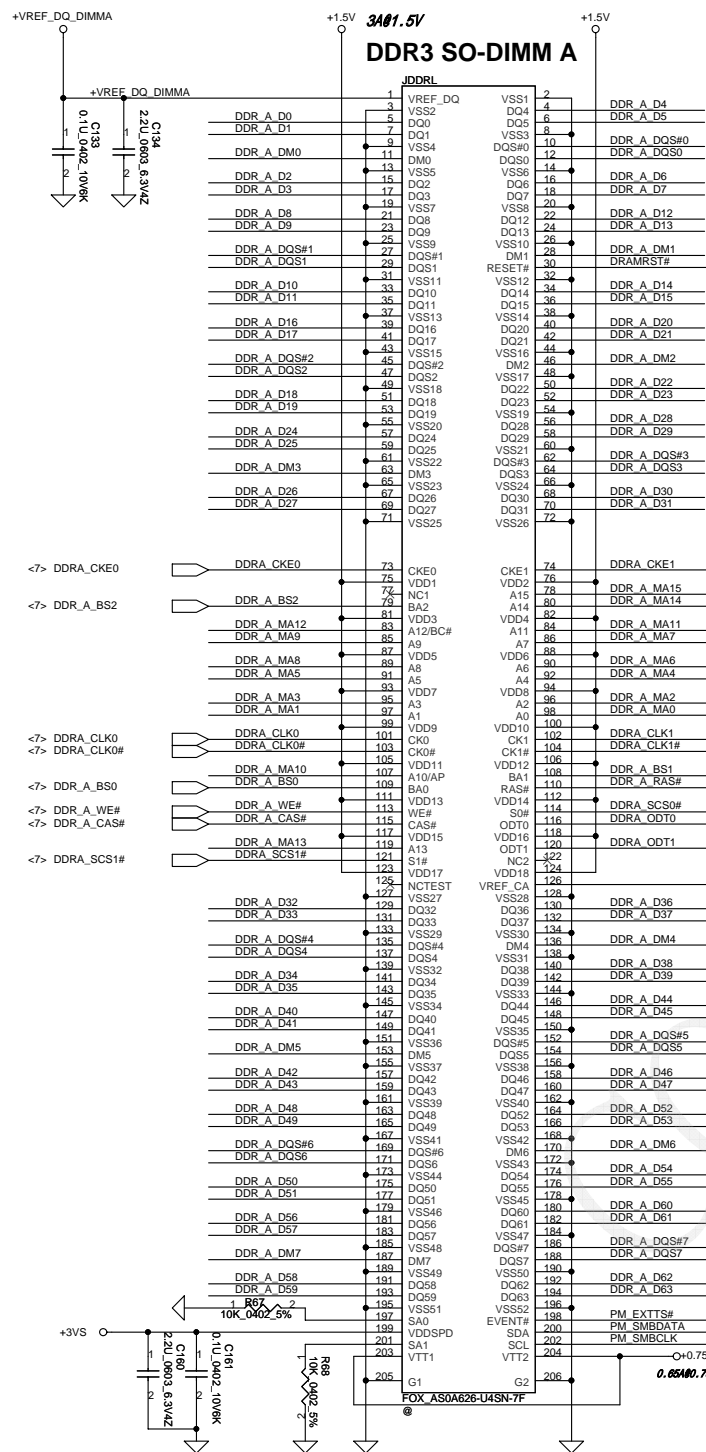




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Document Number				401715				SCHEMATIC, MB A5361			
Date				Monday, January 11, 2010				Sheet 8 of 52			

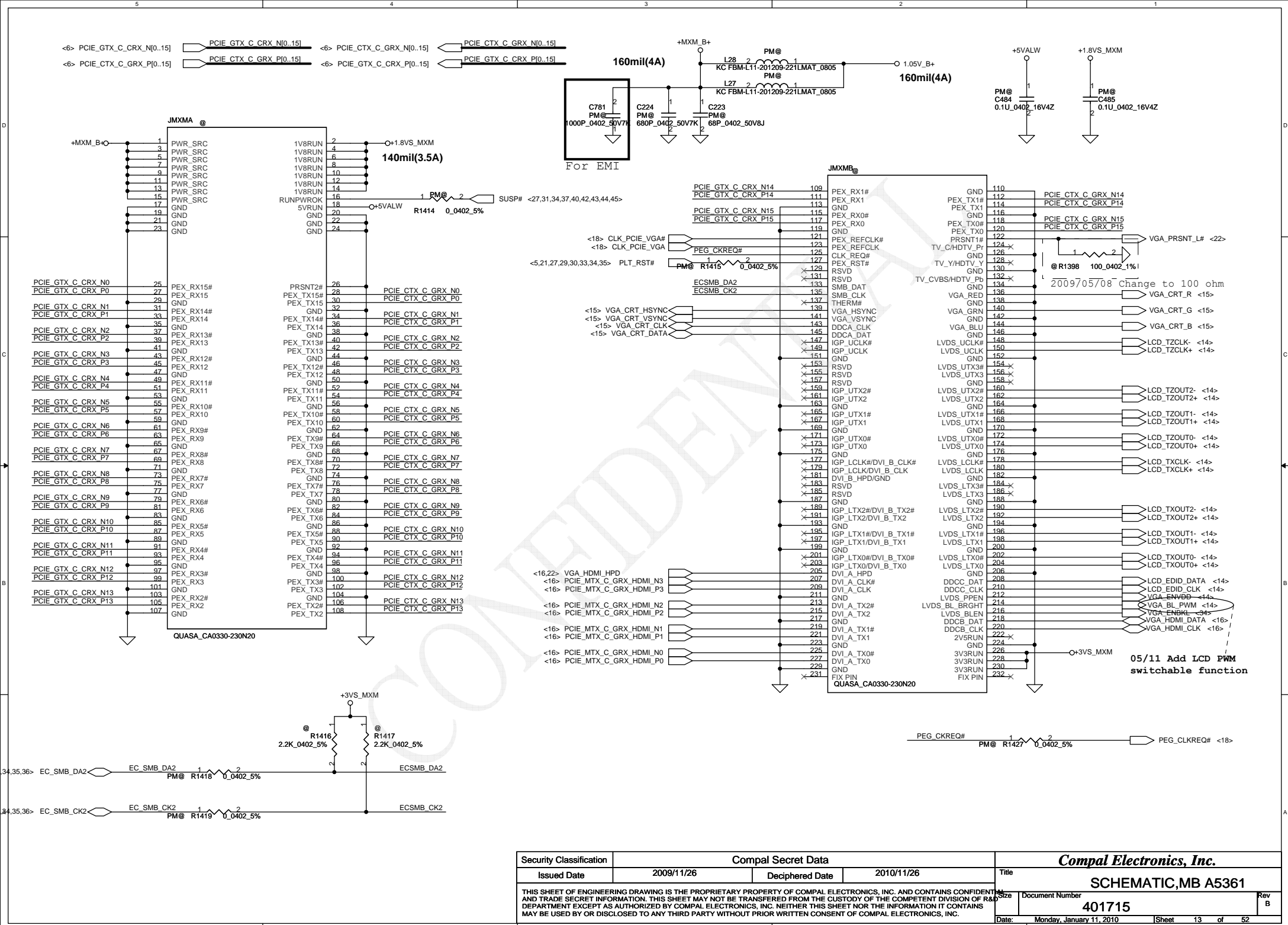






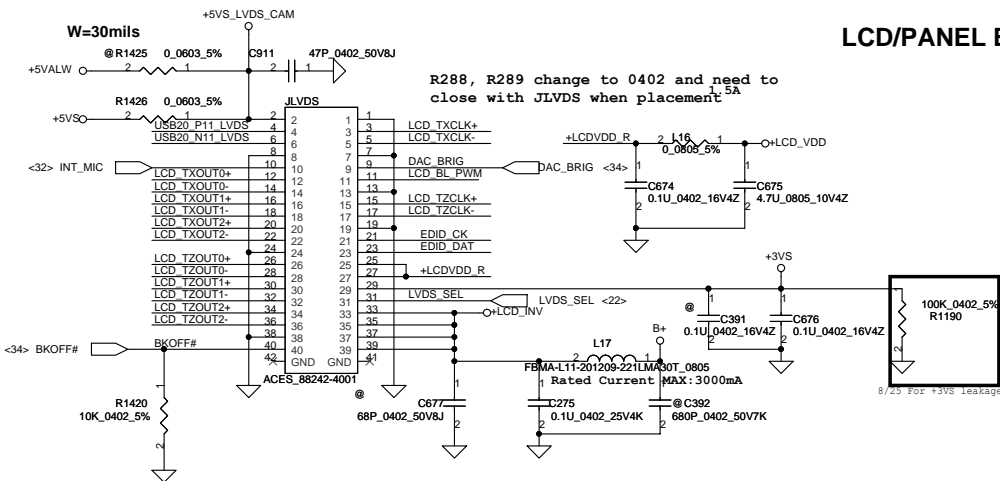






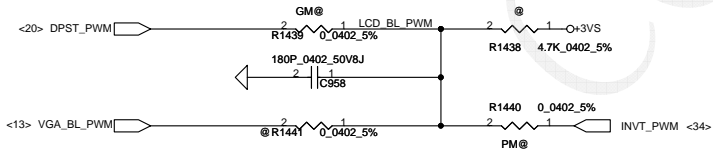
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							401715	B
						Date:	Monday, January 11, 2010	Sheet 13 of 52

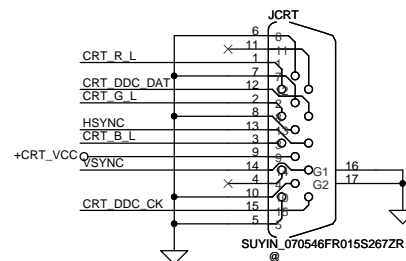
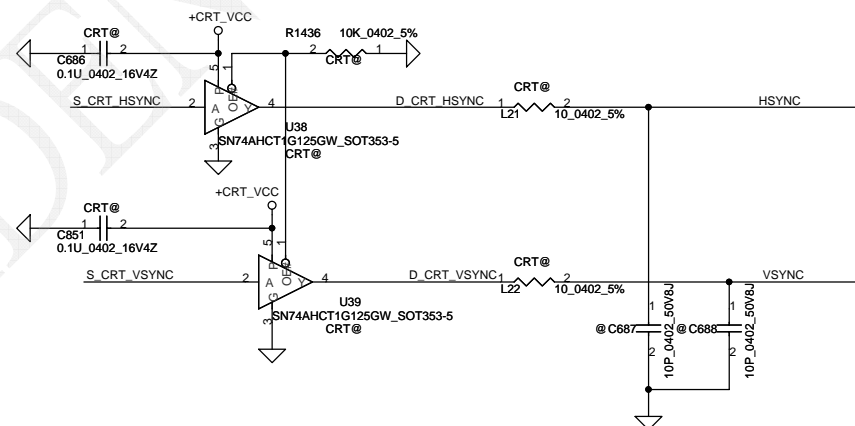
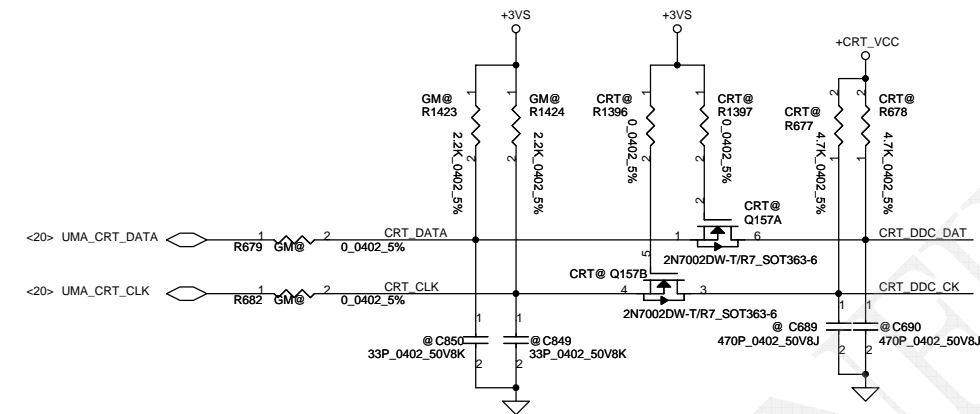
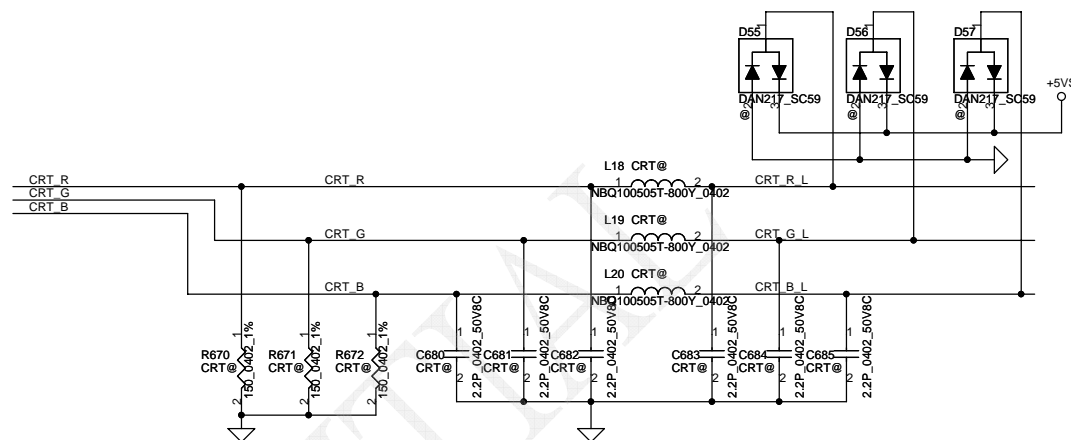
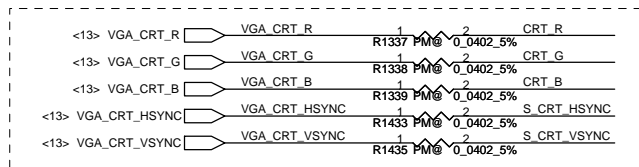
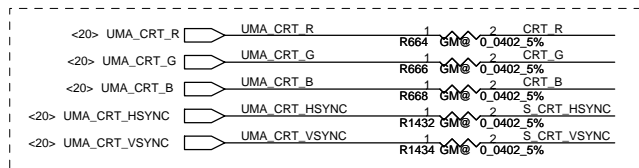
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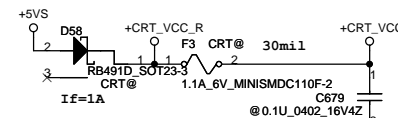
Please near MXM conn for these resistors.

PCH side	MXM conn.
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<20> LVDS_A0N	LVDS A0N R631 1 GM@ 2 0.0402_5% LCD TXOUT0-
<20> LVDS_A1P	LVDS A1P R633 1 GM@ 2 0.0402_5% LCD TXOUT1+
<20> LVDS_A1N	LVDS A1N R635 1 GM@ 2 0.0402_5% LCD TXOUT1-
<20> LVDS_A2P	LVDS A2P R637 1 GM@ 2 0.0402_5% LCD TXOUT2+
<20> LVDS_A2N	LVDS A2N R639 1 GM@ 2 0.0402_5% LCD TXOUT2-
<20> LVDS_ACLKP	LVDS ACLKP R641 1 GM@ 2 0.0402_5% LCD TXCLK+
<20> LVDS_ACLKN	LVDS ACLKN R643 1 GM@ 2 0.0402_5% LCD TXCLK-
<20> LVDS_B0P	LVDS B0P R645 1 GM@ 2 0.0402_5% LCD TZOUT0+
<20> LVDS_B0N	LVDS B0N R647 1 GM@ 2 0.0402_5% LCD TZOUT0-
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<20> LVDS_B1N	LVDS B1N R651 1 GM@ 2 0.0402_5% LCD TZOUT1-
<20> LVDS_B2P	LVDS B2P R653 1 GM@ 2 0.0402_5% LCD TZOUT2+
<20> LVDS_B2N	LVDS B2N R655 1 GM@ 2 0.0402_5% LCD TZOUT2-
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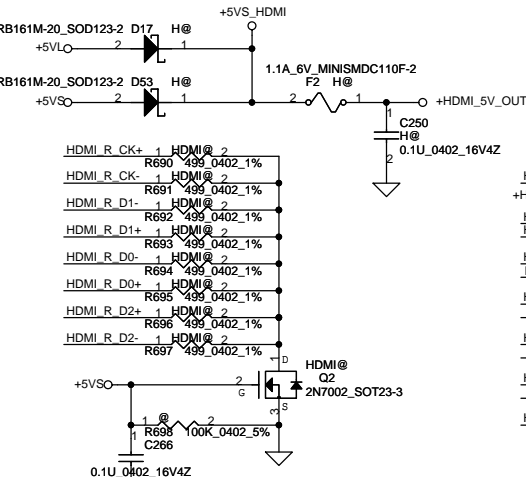
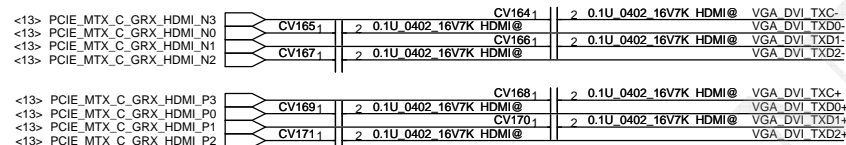
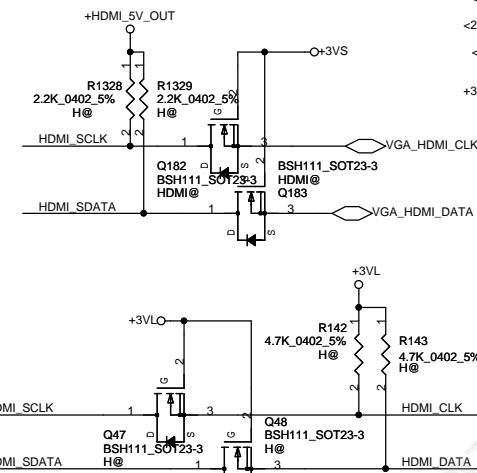
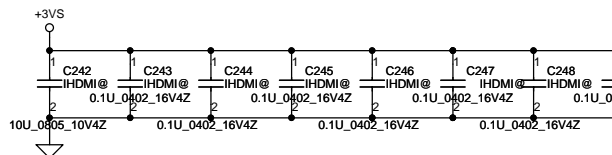
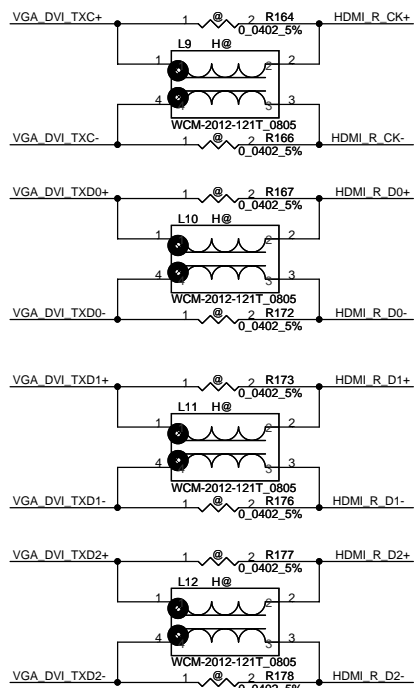




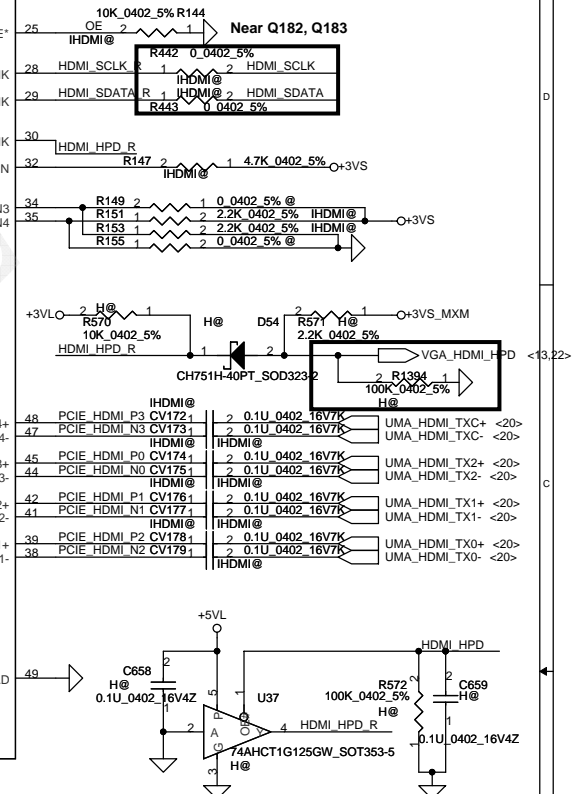
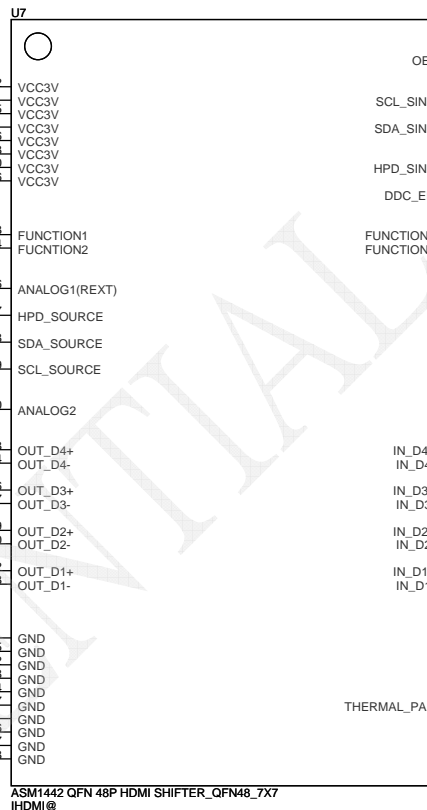
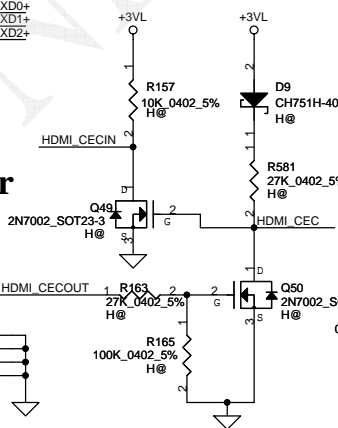
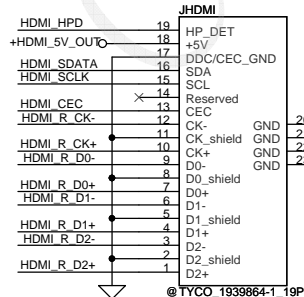
## CRT CONNECTOR



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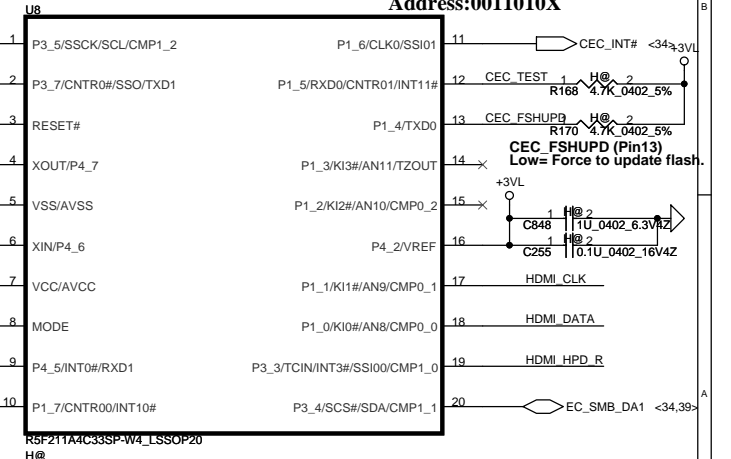


## HDMI Connector



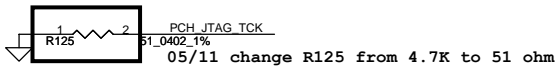
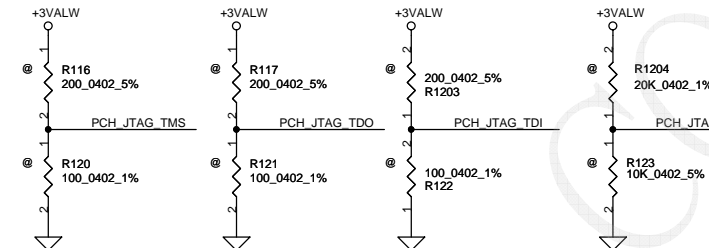
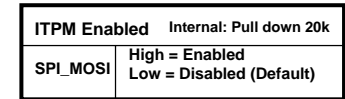
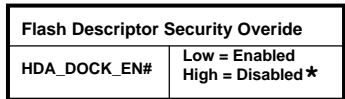
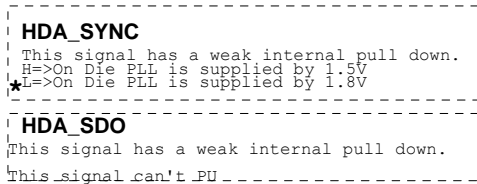
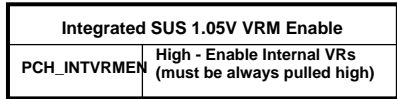
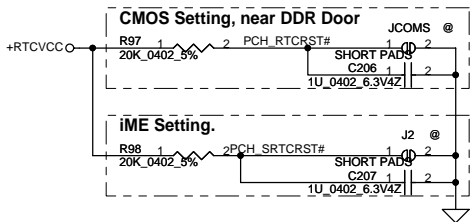
## HDMI CEC Controller

Address:0011010X

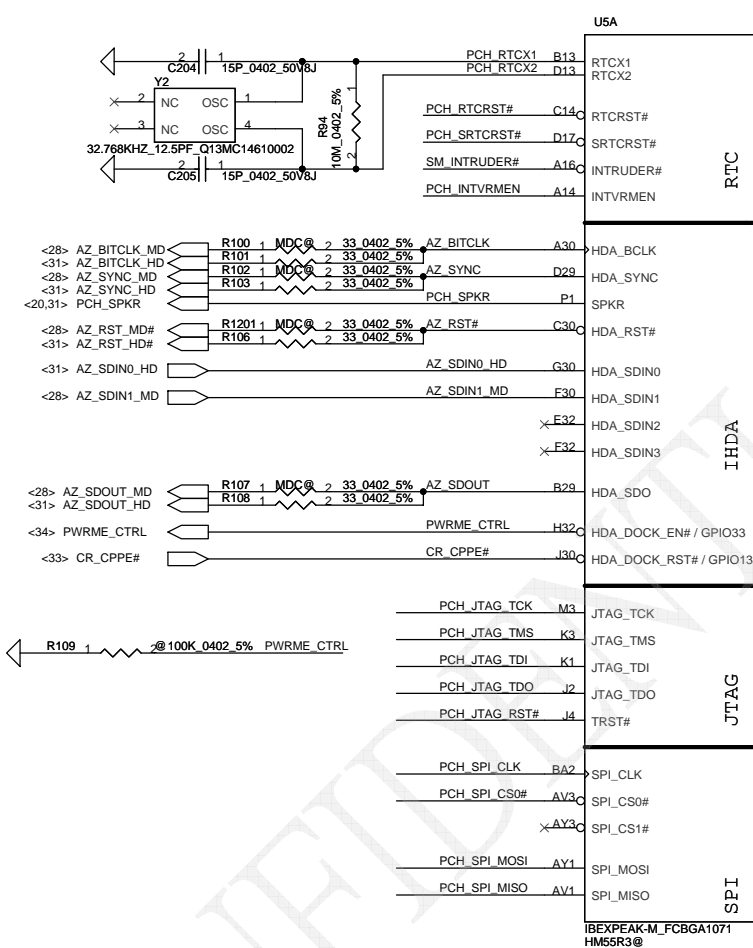


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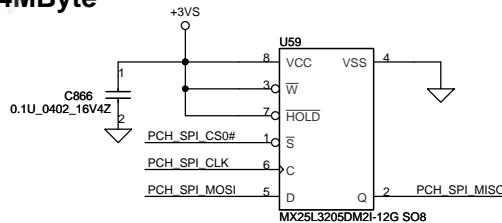




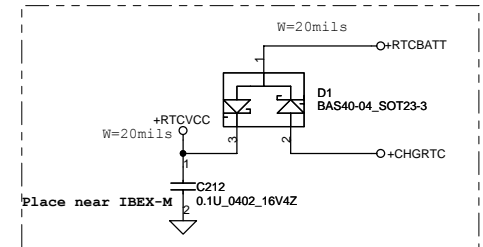
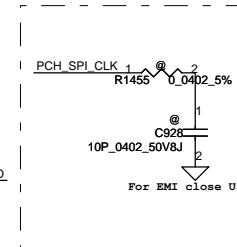
PCH Pin	RefDes	PCH JTAG Enable	PCH JTAG Disable
PCH_JTAG_TDO	R358	No Install	No Install
PCH_JTAG_TMS	R355	No Install	No Install
PCH_JTAG_TDI	R354	No Install	No Install
PCH_JTAG_RST#	R353	No Install	No Install



## SPI ROM For Basic ME ROM size (w/o Braidwood & system BIOS): 4MByte



0915: P/N: SA000010Z00 & SA000021A00  
9/8 PVT change to 4MB for SW demand



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New Card

WLAN

LAN

UWB

Card Reader

TV

New Card

WLAN

LAN

UWB

Card Reader

TV

USB

PCI-E\*

Controller

Link

PEG

From CLK BUFFER

Clock Flex

SMBALERT# / GPIO11  
SMBCLK  
SMBDATA  
SML0ALERT# / GPIO60  
SML0CLK  
SML0DATA  
SML1ALERT# / GPIO74  
SML1CLK / GPIO58  
SML1DATA / GPIO75

CL\_CLK1#  
CL\_DATA1  
CL\_RST1#

PEG\_A\_CLKRQ# / GPIO47

CLKOUT\_PEG\_A\_N  
CLKOUT\_PEG\_A\_P

CLKOUT\_DMI\_N  
CLKOUT\_DMI\_P

CLKOUT\_DP\_N / CLKOUT\_BCLK1\_N  
CLKOUT\_DP\_P / CLKOUT\_BCLK1\_P

CLKIN\_DMI\_N  
CLKIN\_DMI\_P

CLKIN\_BCLK\_N  
CLKIN\_BCLK\_P

CLKIN\_DOT\_96N  
CLKIN\_DOT\_96P

CLKIN\_SATA\_N / CKSSCD\_N  
CLKIN\_SATA\_P / CKSSCD\_P

REFCLK14IN

CLKIN\_PCILOOPBACK

XTAL25\_IN  
XTAL25\_OUT

XCLK\_RCOMP

CLKOUTFLEX0 / GPIO64

CLKOUTFLEX1 / GPIO65

CLKOUTFLEX2 / GPIO66

CLKOUTFLEX3 / GPIO67

B9 EC\_LID\_OUT#  
H14 PCH\_SMBCLK  
C8 PCH\_SMBDATA  
H14 PCH\_GPIO60  
C6 PCH\_SMLCLK0  
G8 PCH\_SMLDATA0  
M14 PCH\_GPIO74  
F10 PCH\_SMLCLK1  
G12 PCH\_SMLDATA1

T13  
T11  
T9

H1 PEG\_CLKREQ#

AD43  
AD45

AN4  
AN2

AT1 CLK\_DP#  
AT3 CLK\_DP

AW24  
BA24

AP3  
AP1

F18  
F18

AH13  
AH12

P41

J42

AH51 PCH\_X1  
AH53 PCH\_X2

AE38 XCLK\_RCOMP

T45

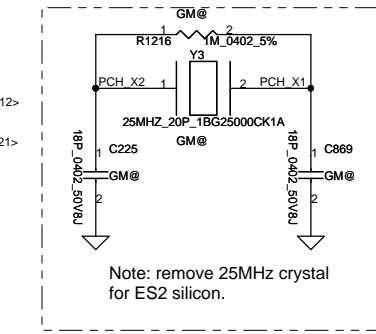
P43

T42

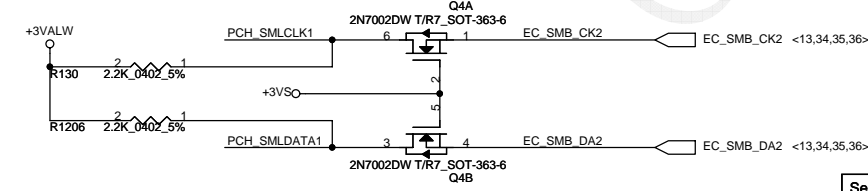
N50 UWB\_OFF#

VGA

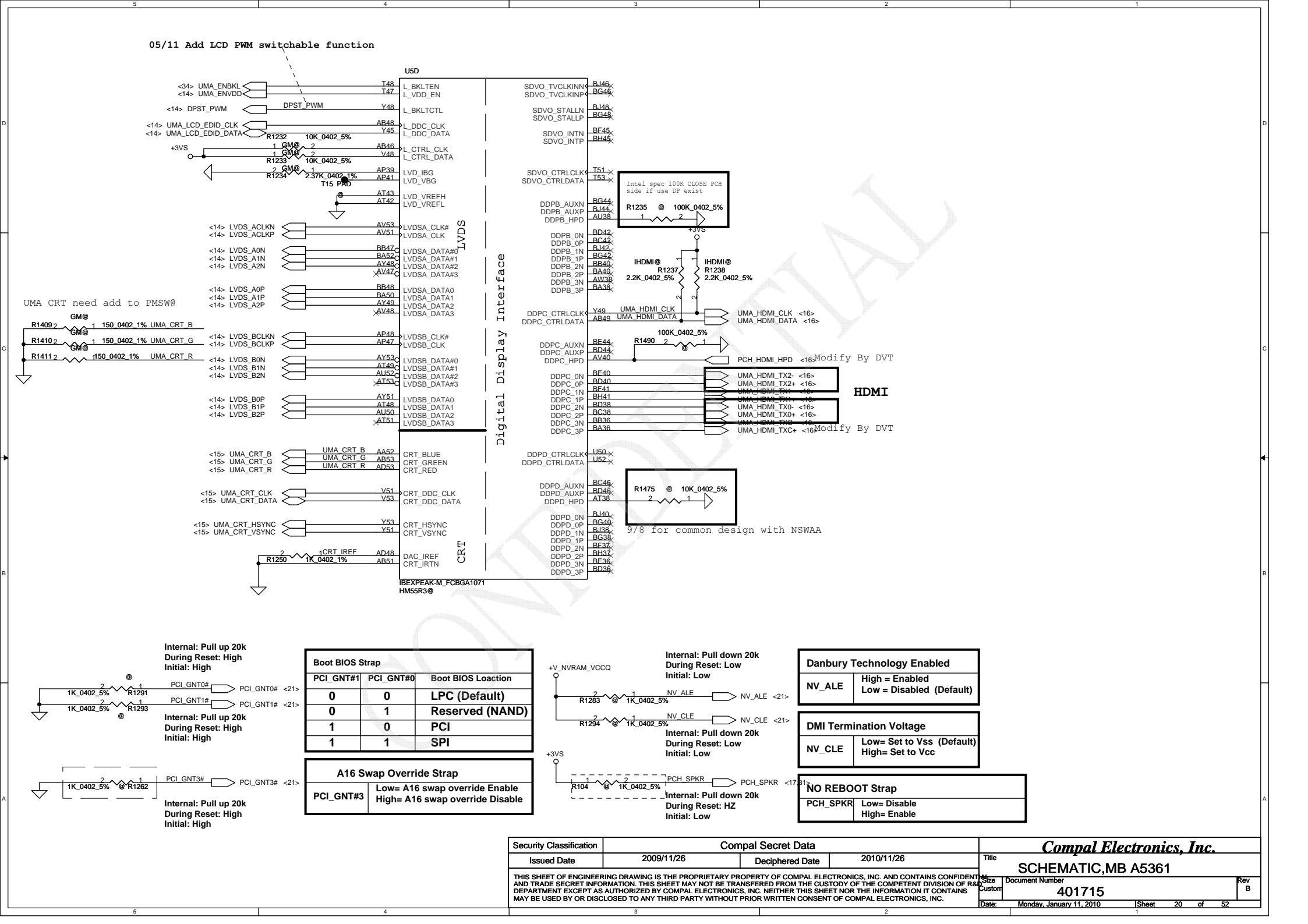
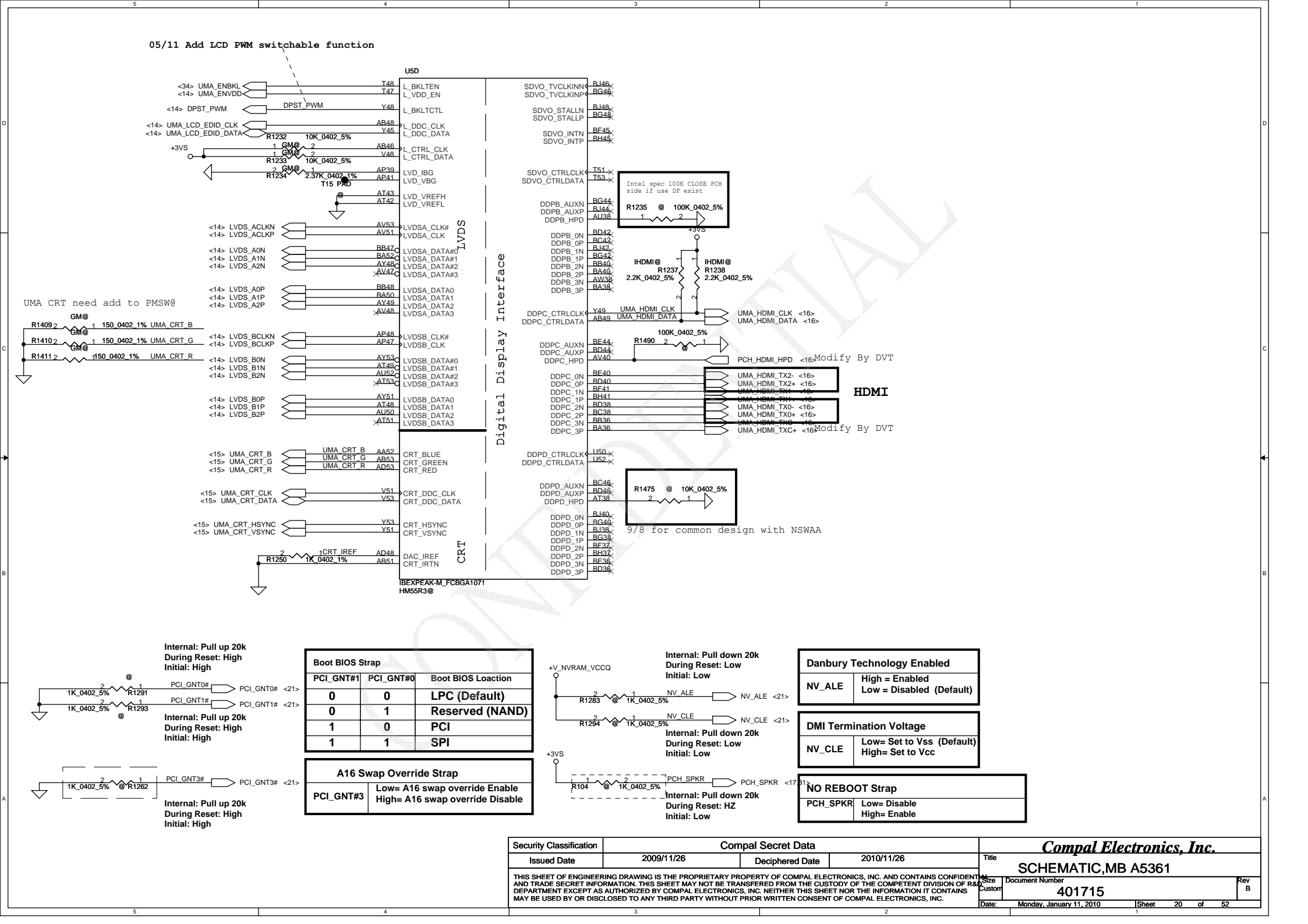
FROM CLK GEN FOR: 133/100/96/14.318 MHZ



Note: Stuff 0 ohm if 25MHz crystal un-stuff





[illegible][illegible][illegible]

05/11 Add LCD PWM switchable function

**Digital Display Interface**

**LVDS**

**HDMI**

**CRT**

**IBEXPEAK-M\_FCBGA1071/HM55R3@**

**Boot BIOS Strap**

PCI_GNT#1	PCI_GNT#0	Boot BIOS Location
0	0	LPC (Default)
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

**A16 Swap Override Strap**

PCI_GNT#3	Low = A16 swap override Enable High = A16 swap override Disable
0	Low = A16 swap override Enable High = A16 swap override Disable

**Danbury Technology Enabled**

NV_ALE	High = Enabled Low = Disabled (Default)
0	High = Enabled Low = Disabled (Default)

**DMI Termination Voltage**

NV_CLE	Low = Set to Vss (Default) High = Set to Vcc
0	Low = Set to Vss (Default) High = Set to Vcc

**NO REBOOT Strap**

PCH_SPKR	Low = Disable High = Enable
0	Low = Disable High = Enable

**Security Classification**

Security Classification	Compal Secret Data
Issued Date	2009/11/26
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**Compal Electronics, Inc.**

**SCHEMATIC, MB A5361**

**Document Number**

**401715**

**Date**

**Monday, January 11, 2010**

**Sheet**

**20**

**of**

**52**

**Rev**

**B**

05/11 Add LCD PWM switchable function

**Digital Display Interface**

**LVDS**

**HDMI**

**CRT**

**Boot BIOS Strap**

PCI_GNT#1	PCI_GNT#0	Boot BIOS Location
0	0	LPC (Default)
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

**A16 Swap Override Strap**

PCI_GNT#3	Low= A16 swap override Enable High= A16 swap override Disable
0	Low= A16 swap override Enable High= A16 swap override Disable

**Security Classification**

Compal Secret Data

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Document Number: 401715

Date: Monday, January 11, 2010

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Rev B

05/11 Add LCD PWM switchable function

UMA CRT need add to PMSW@

Intel spec 100K CLOSE PCH side if use DP exist

9/8 for common design with NSWAA

Internal: Pull up 20k  
During Reset: High  
Initial: High

Internal: Pull up 20k  
During Reset: High  
Initial: High

Internal: Pull up 20k  
During Reset: High  
Initial: High

PCI_GNT#1	PCI_GNT#0	Boot BIOS Location
0	0	LPC (Default)
0	1	Reserved (NAND)
1	0	PCI
1	1	SPI

A16 Swap Override Strap	
PCI_GNT#3	Low= A16 swap override Enable High= A16 swap override Disable

Internal: Pull down 20k  
During Reset: Low  
Initial: Low

Internal: Pull down 20k  
During Reset: Low  
Initial: Low

Internal: Pull down 20k  
During Reset: Low  
Initial: Low

Internal: Pull down 20k  
During Reset: Low  
Initial: Low

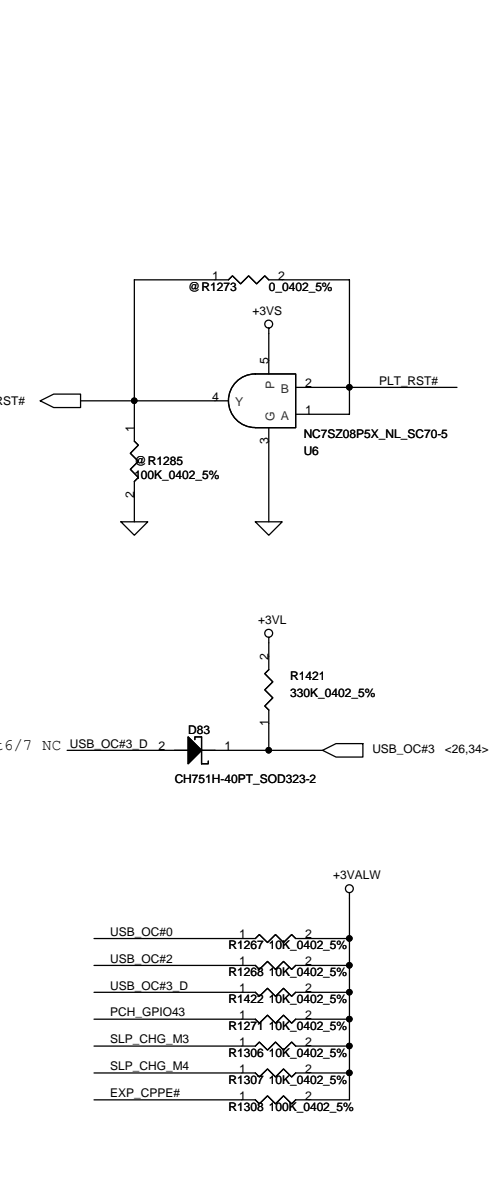
Danbury Technology Enabled	
NV_ALE	High = Enabled Low = Disabled (Default)

DMI Termination Voltage	
NV_CLE	Low= Set to Vss (Default) High= Set to Vcc

NO REBOOT Strap	
PCH_SPKR	Low= Disable High= Enable

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[illegible]

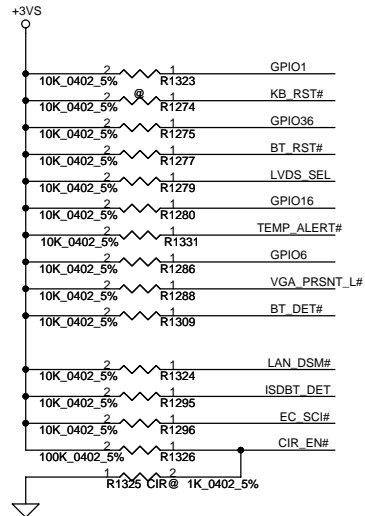
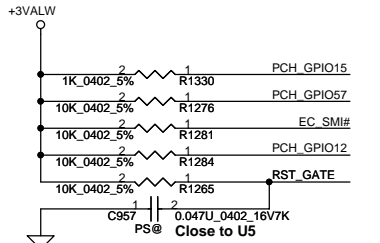


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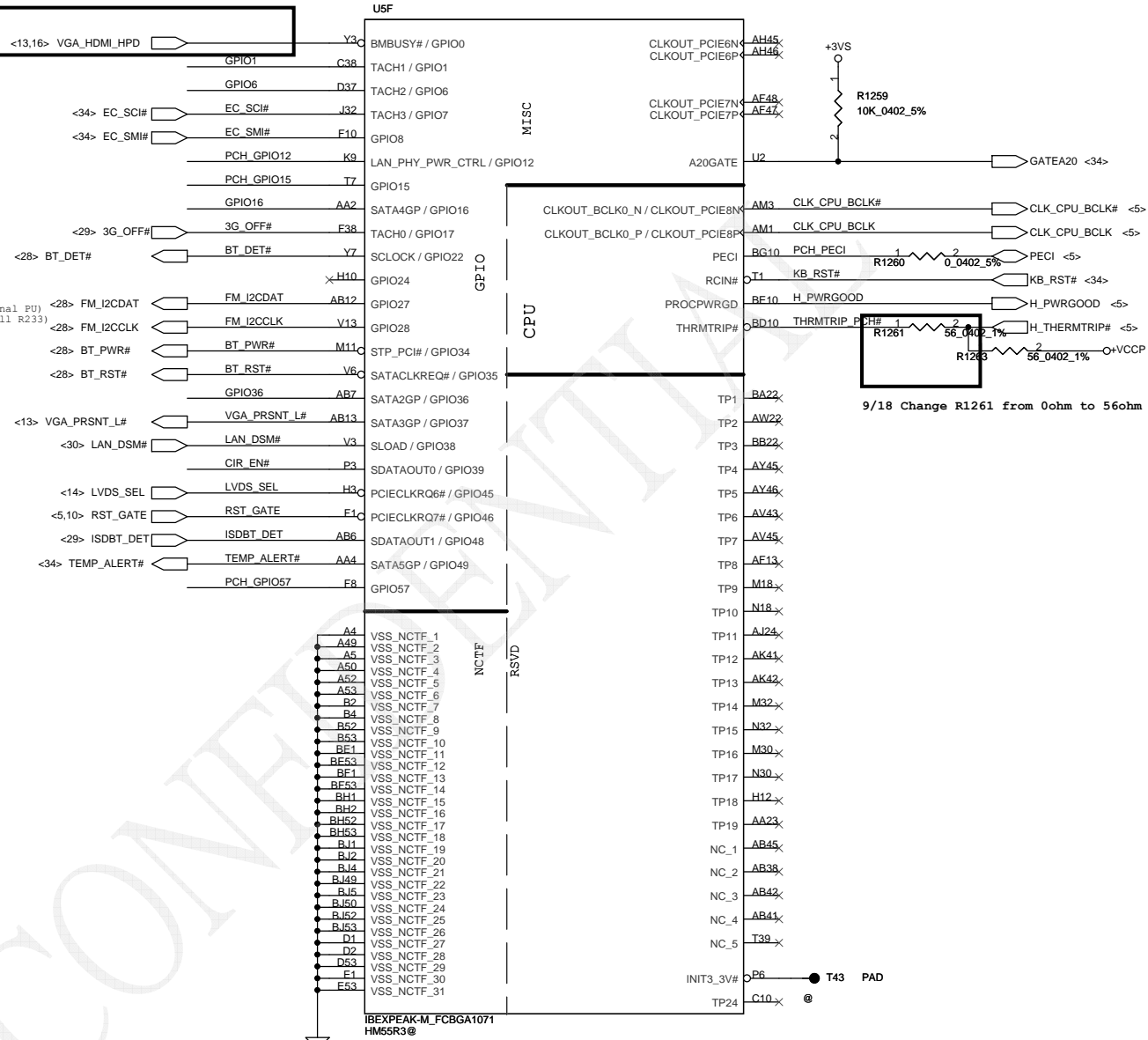
GPIO15  
L: Intel ME Crypto Transport  
Layer Security(TLS) chiper suite  
with no confidentiality  
H: Intel ME Crypto Transport  
Layer Security(TLS) chiper suite  
with confidentiality  
It have weak internal PU 20K

GPIO27  
On-Die PLL Voltage Regulator  
This signal has a weak internal pull up  
\* H: On-Die voltage regulator enable  
L: On-Die PLL Voltage Regulator disable

GPIO8  
This signal has a weak internal pull up  
can't Pull low

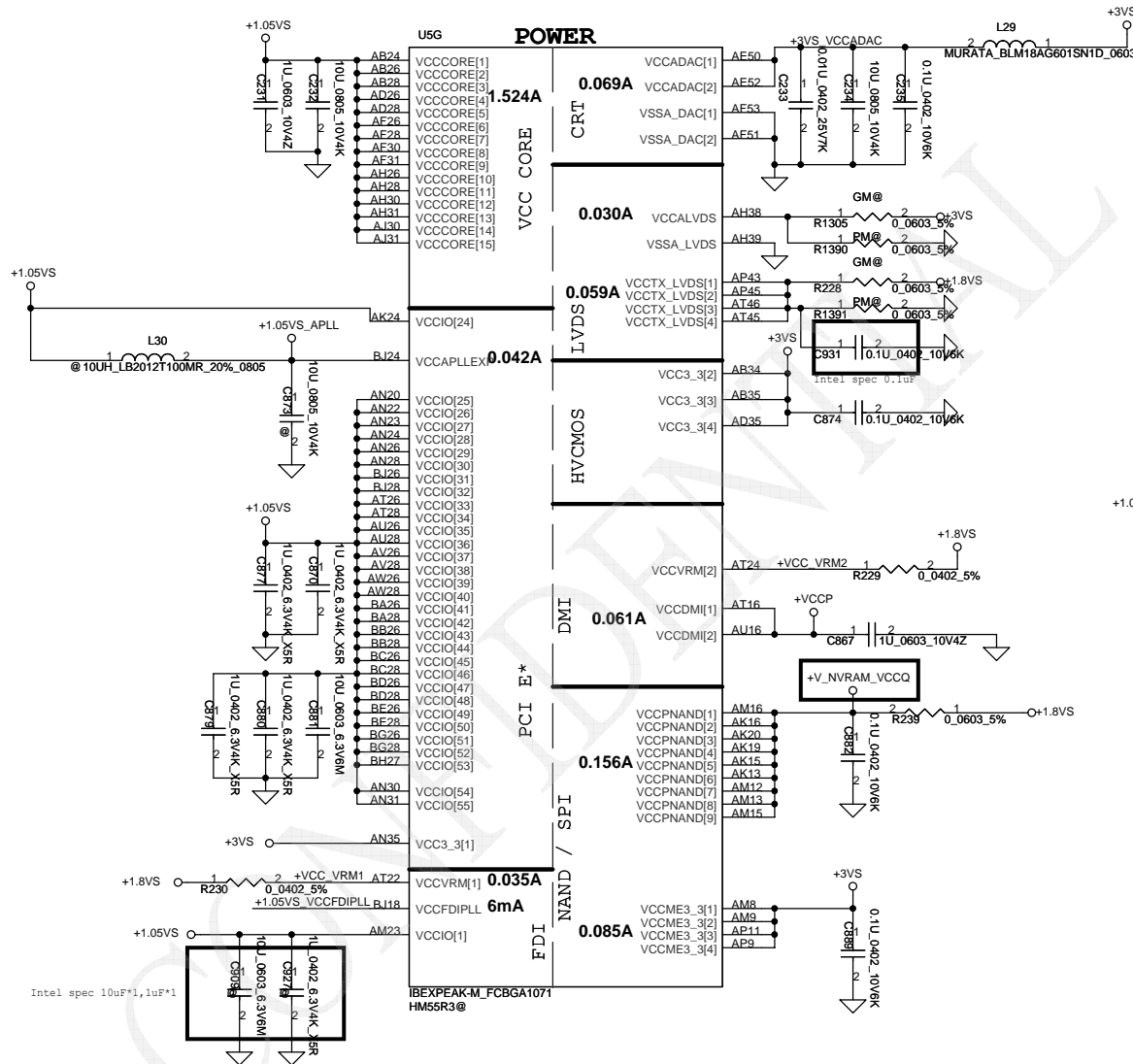


GPIO27(on-die VR)  
\*Enable: 1 (internal PU)  
Disable: 0 (install R233)



9/18 Change R1261 from 0ohm to 56ohm

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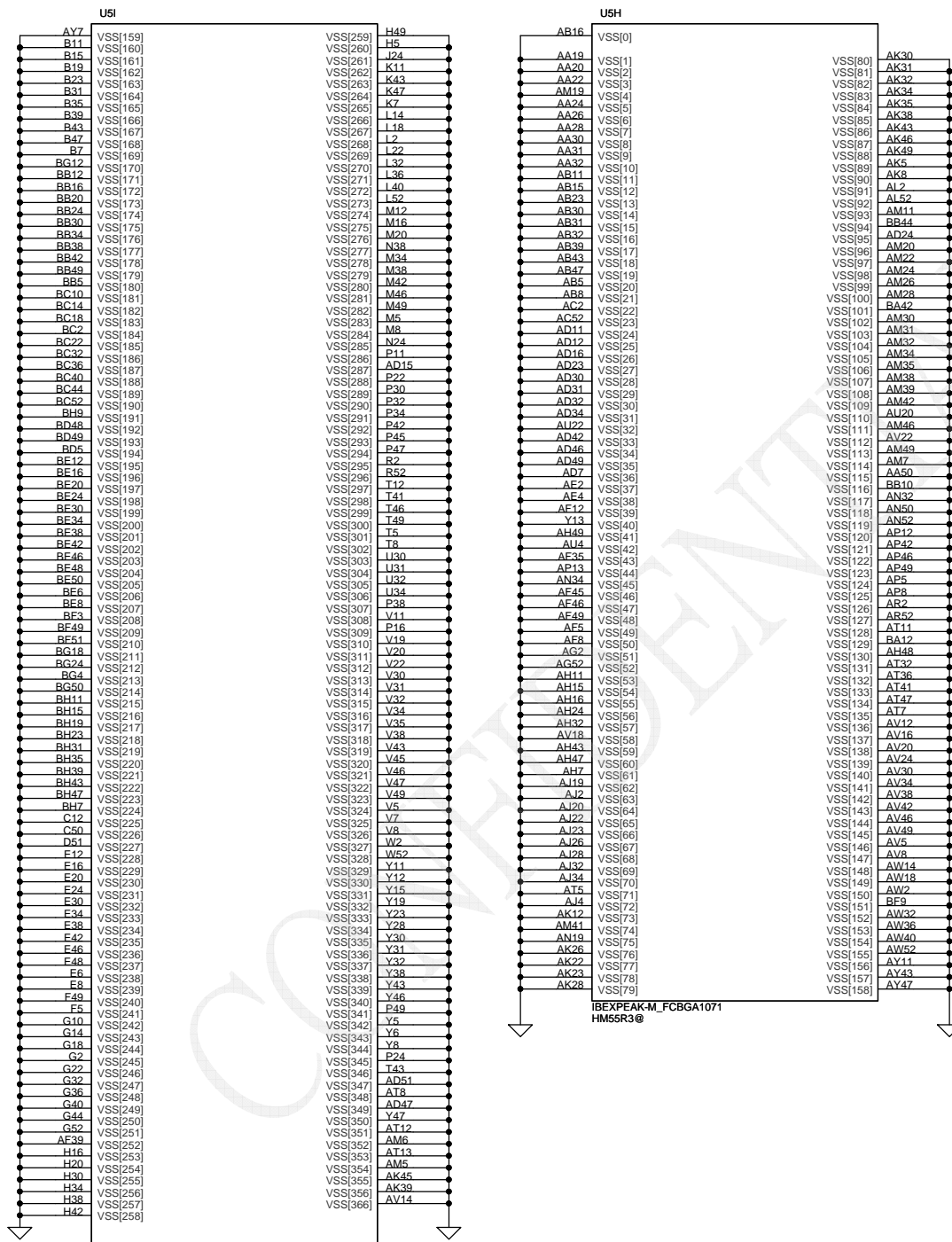
VccpNAND

1.8 V supply for Dual Channel NAND interface. This power is supplied by core well. If unused, this pin can be left as no connect.

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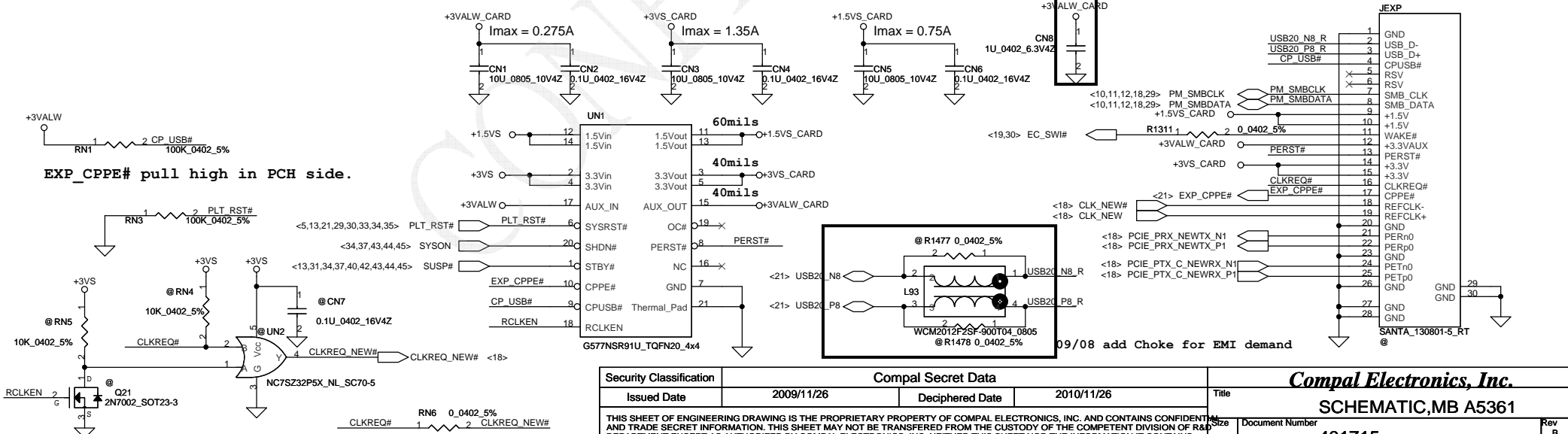
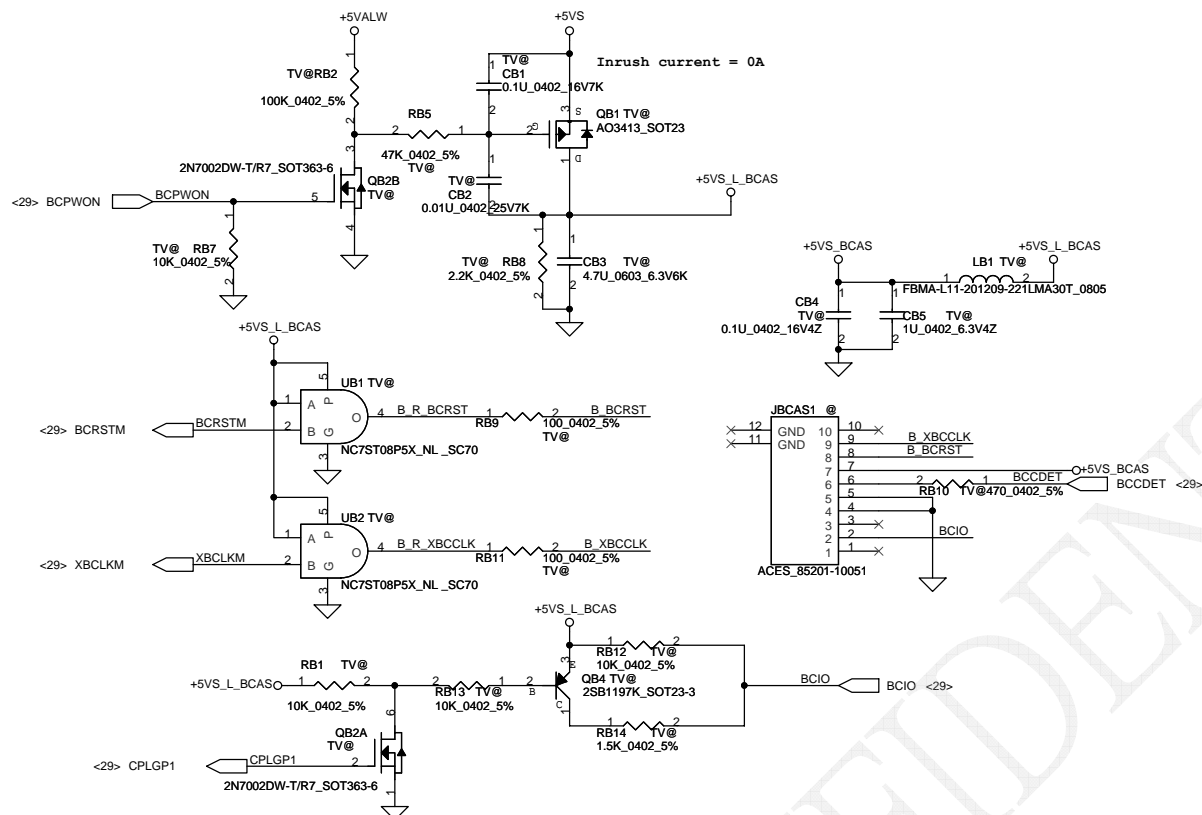


IBEXPEAK-M\_FCBGA1071  
HM55R3@

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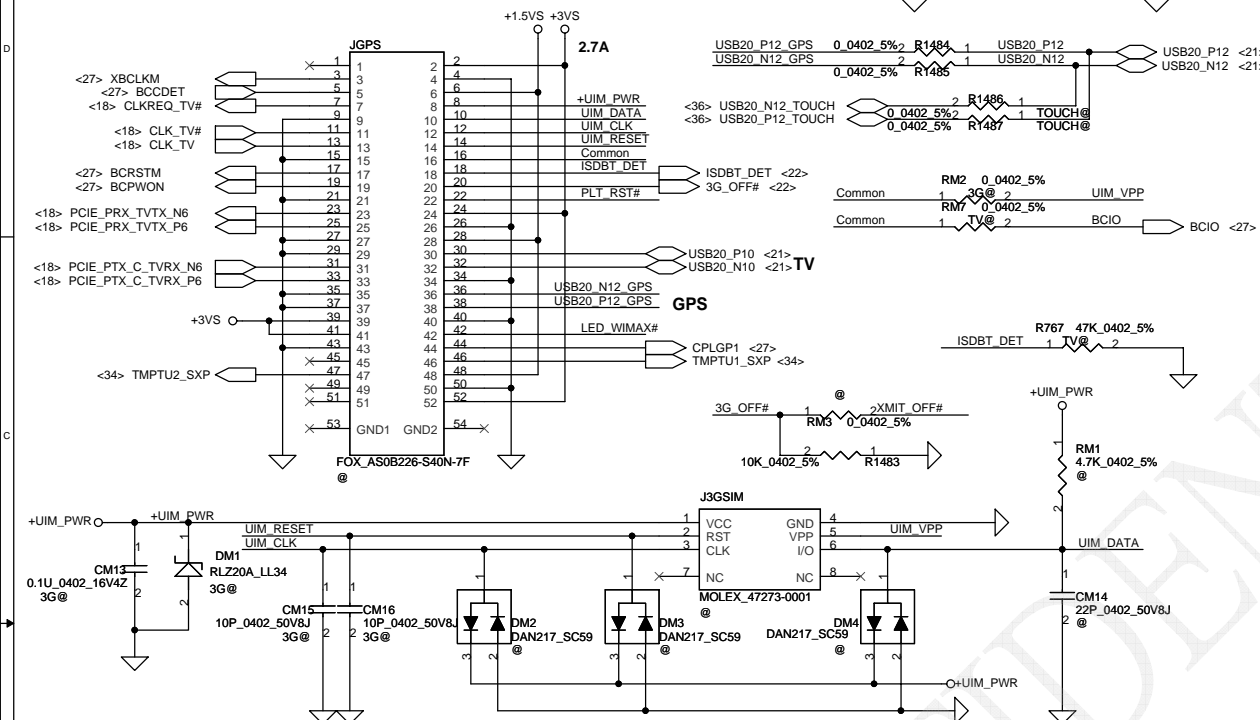
# B-CAS Circuit



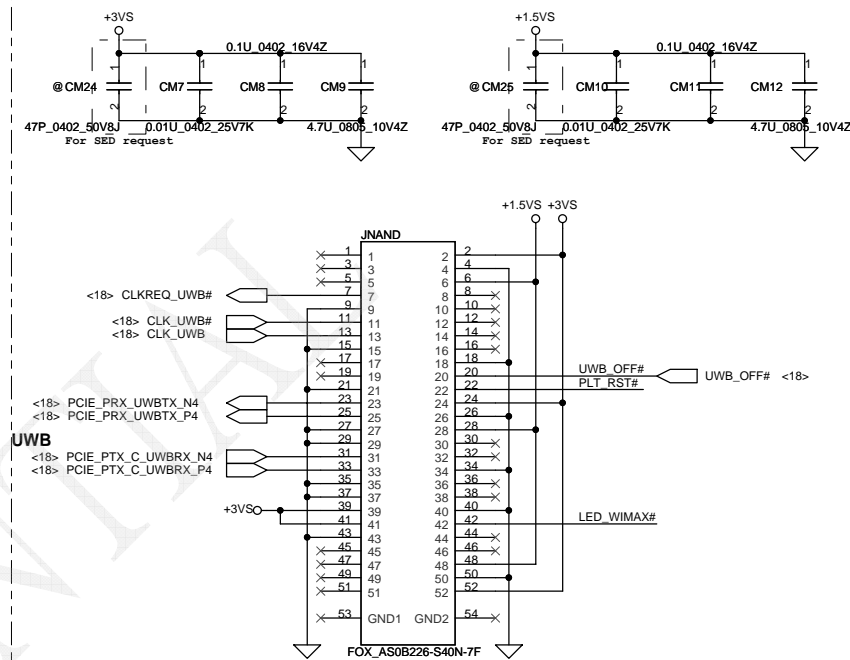
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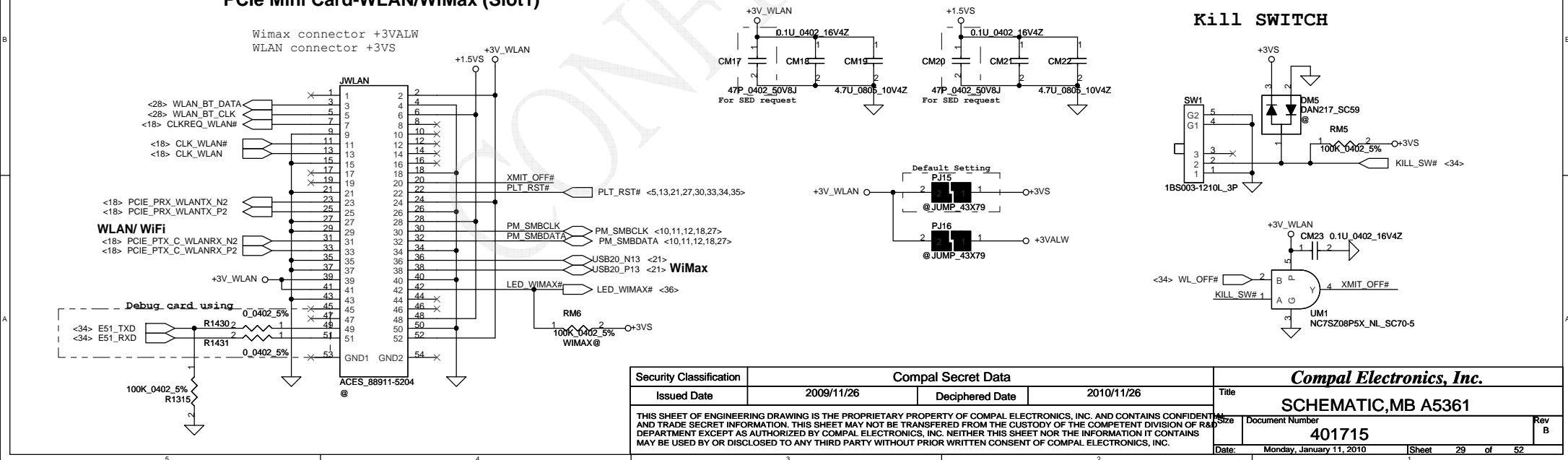
### PCIe Mini Card-3G/GPS/TV Tuner (Slot2)



### UWB/JET (Slot3)



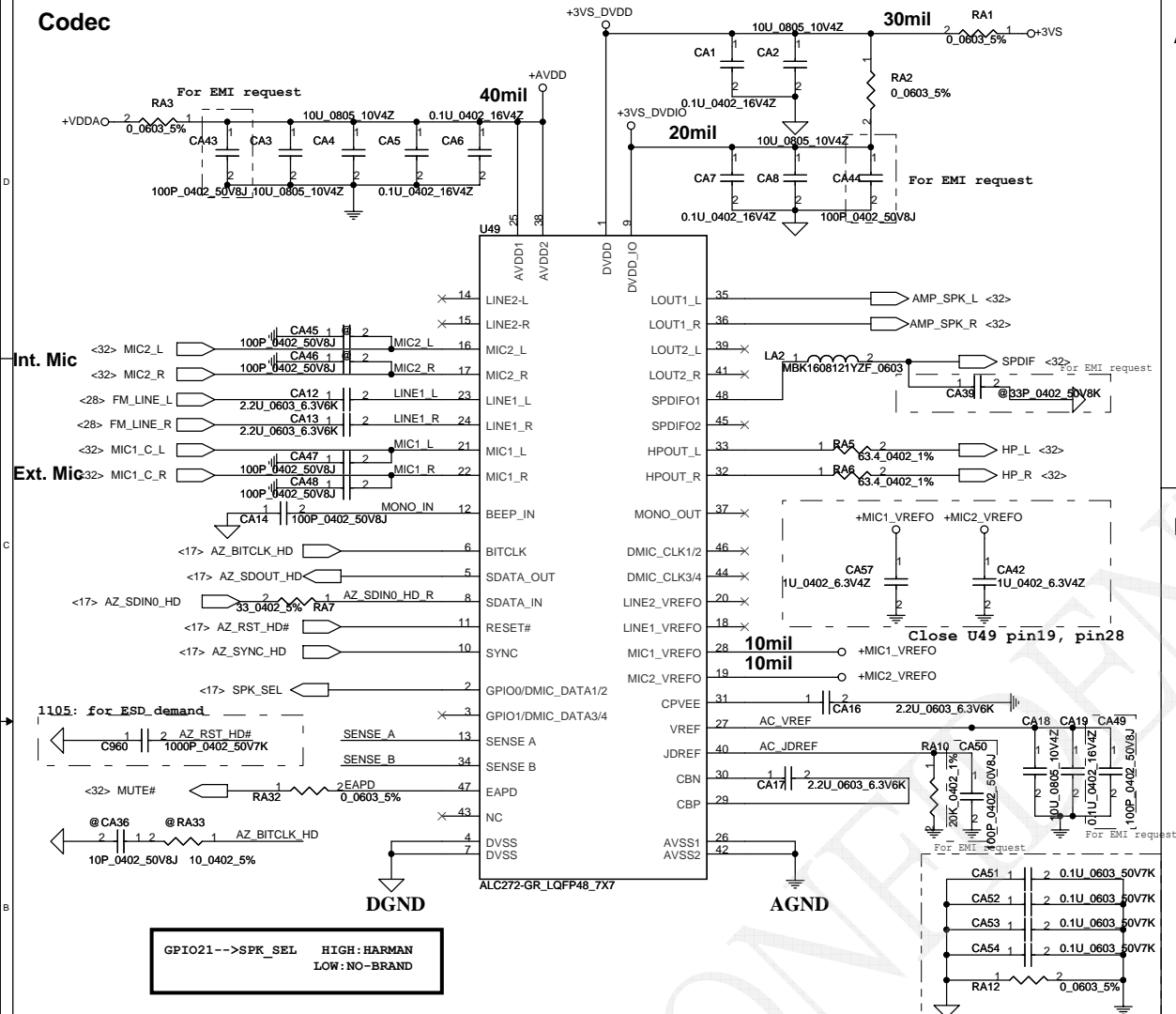
### PCIe Mini Card-WLAN/WiMax (Slot1)



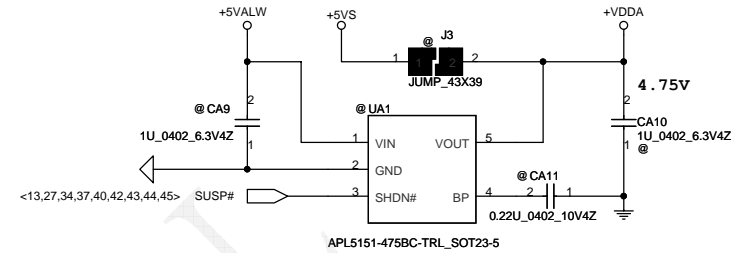
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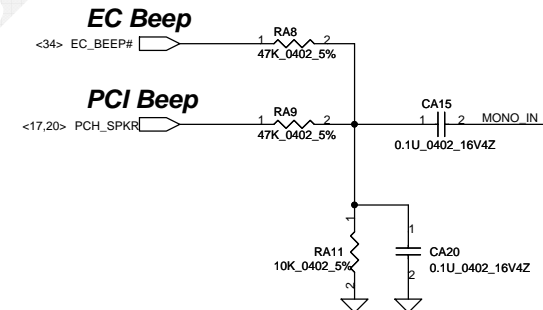
## Codec



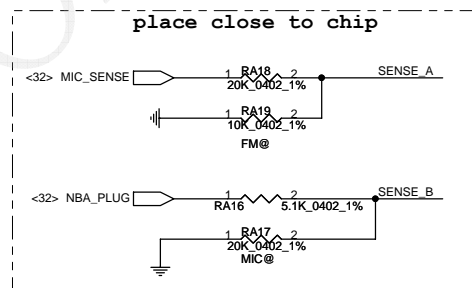
## Audio regulator



### Beep sound

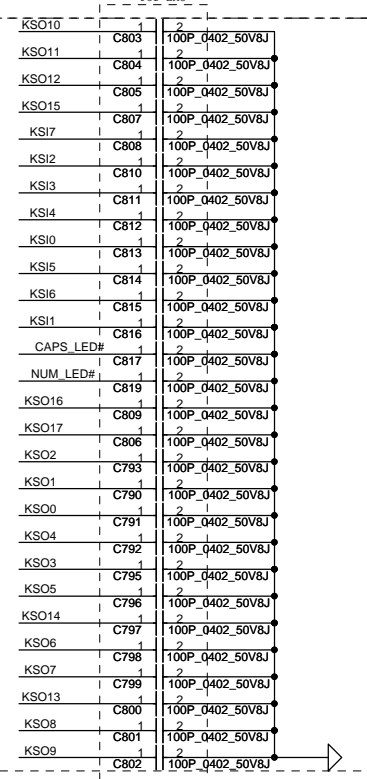
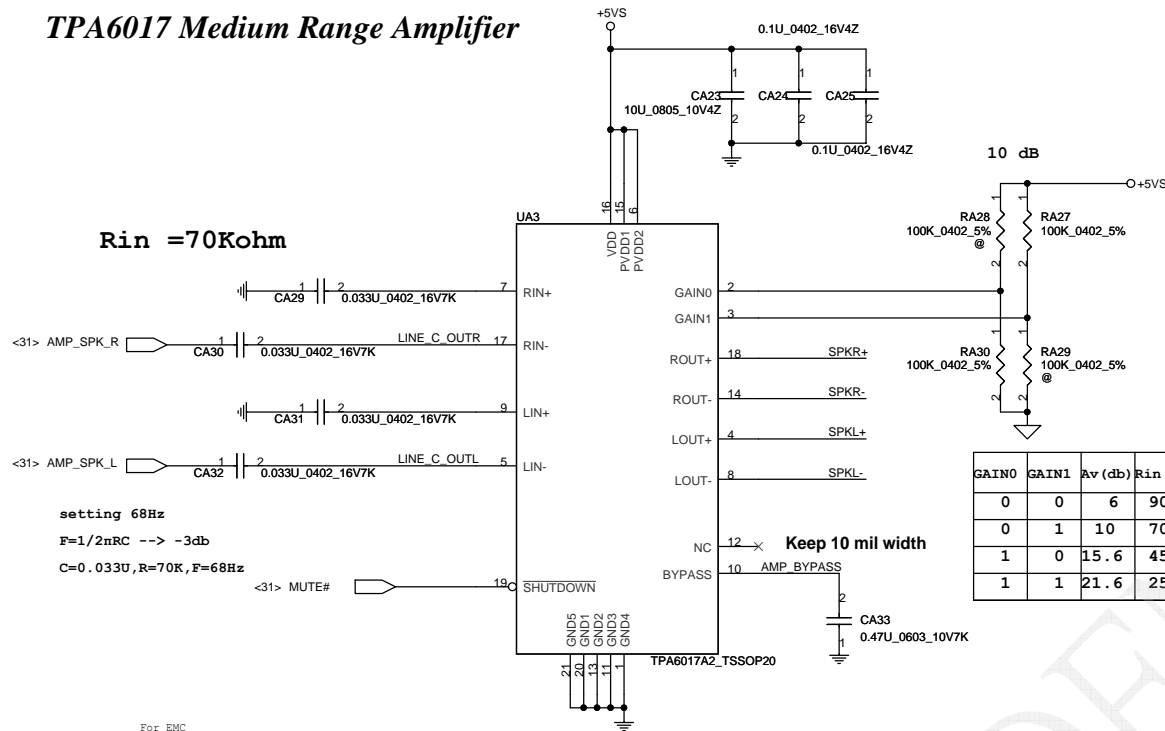


Sense Pin	Impedance	Codec Signals	Function
SENSE A	39.2K	PORT-A (PIN 39, 41)	
	20K	PORT-B (PIN 21, 22)	Ext. MIC
	10K	PORT-C (PIN 23, 24)	FM tuner
	5.1K	PORT-D (PIN 35, 36)	SPK out
SENSE B	39.2K	PORT-E (PIN 14, 15)	
	20K	PORT-F (PIN 16, 17)	Int. MIC
	10K	PORT-H (PIN 37)	
	5.1K	PORT-I (PIN 32, 33)	Headphone out

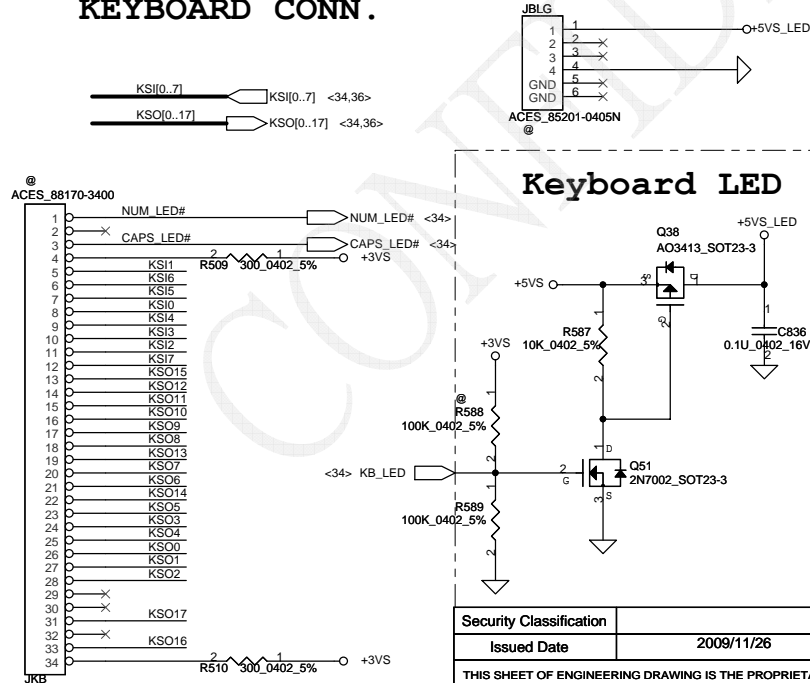


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# TPA6017 Medium Range Amplifier

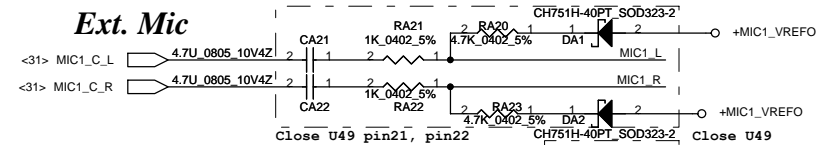


## KEYBOARD CONN.

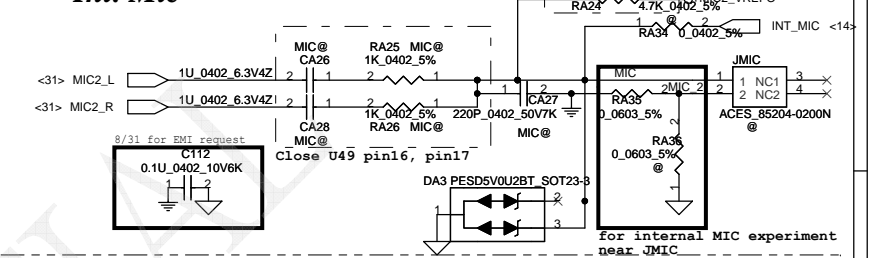


## Keyboard LED

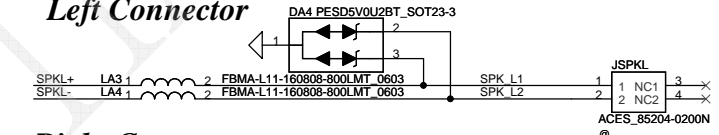
## Ext. Mic



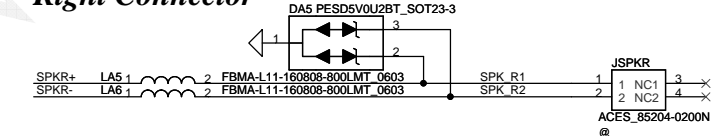
## Int. Mic



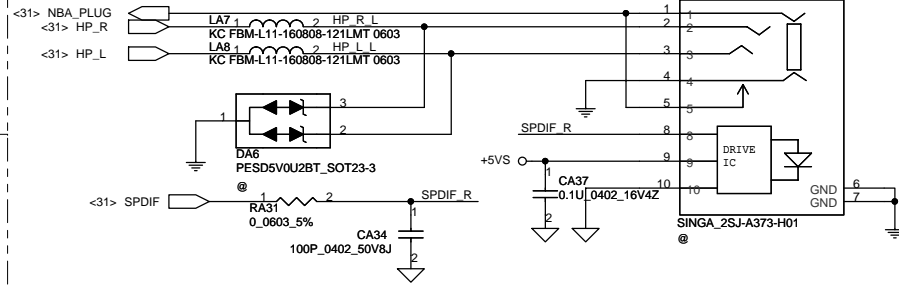
## Left Connector



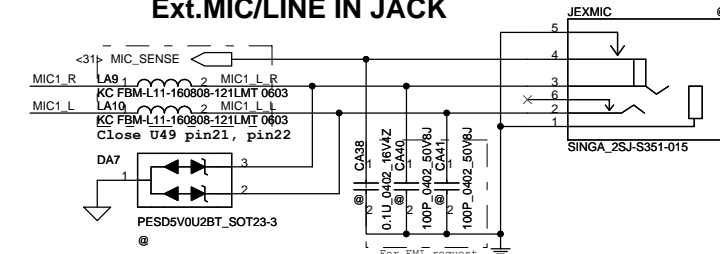
## Right Connector



## HeadPhone/LINE Out JACK



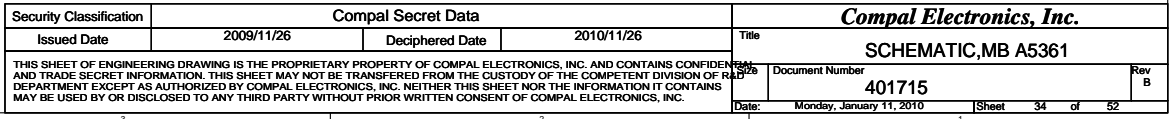
## Ext.MIC/LINE IN JACK



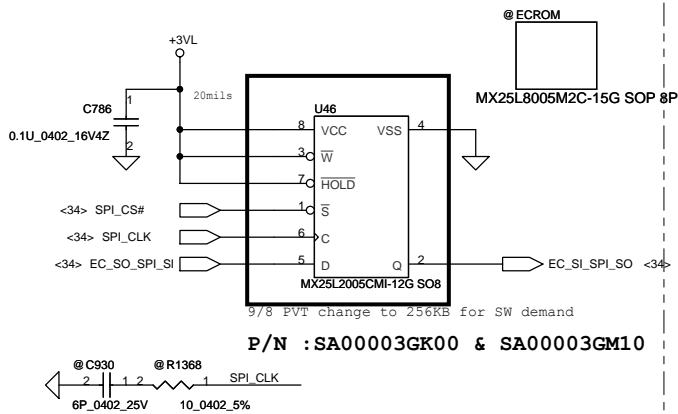
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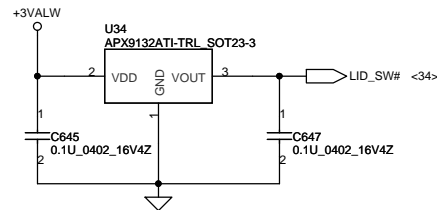




## SPI Flash (1MByte\*1)

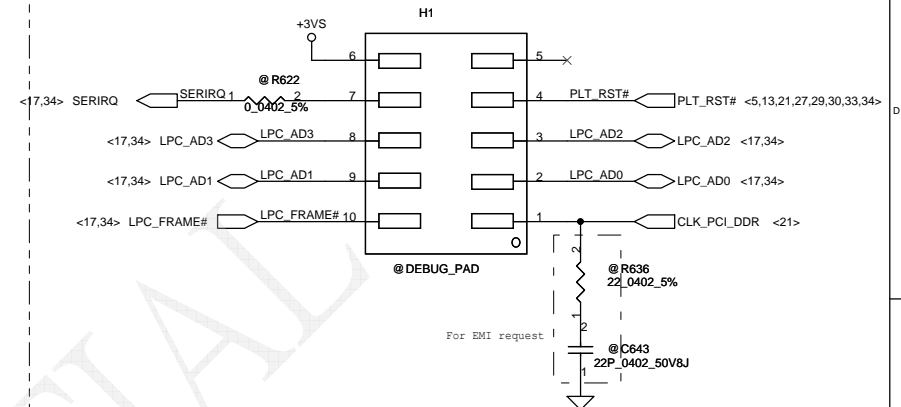


## Lid SW

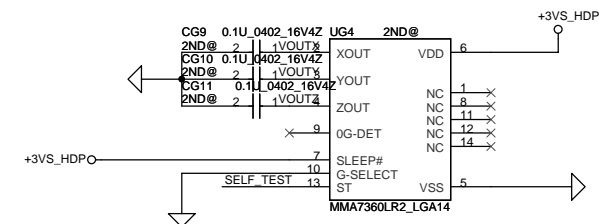
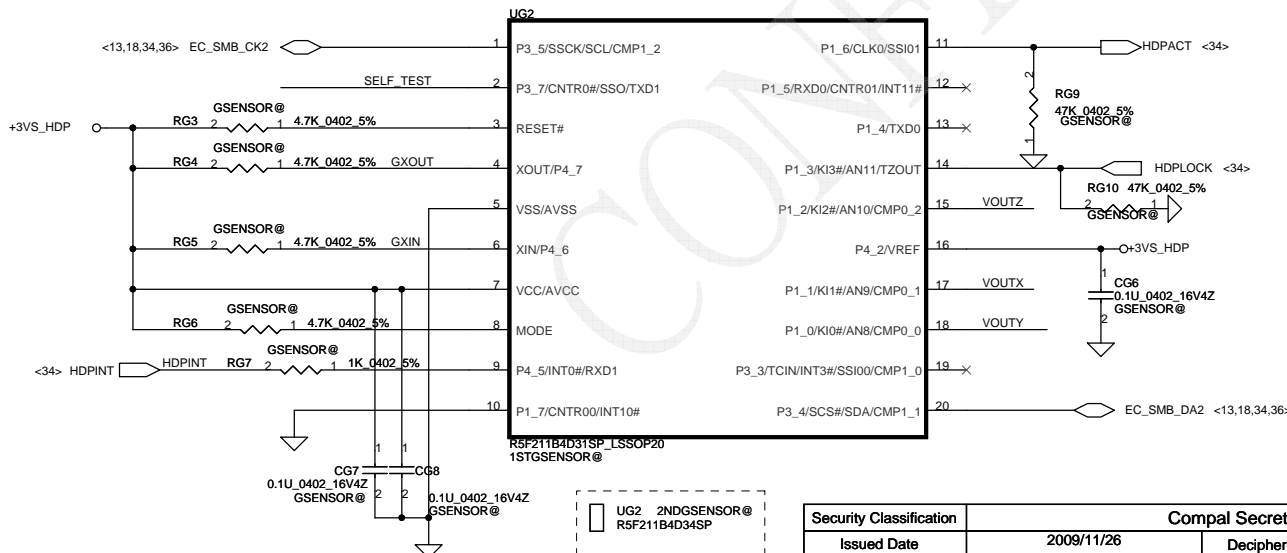
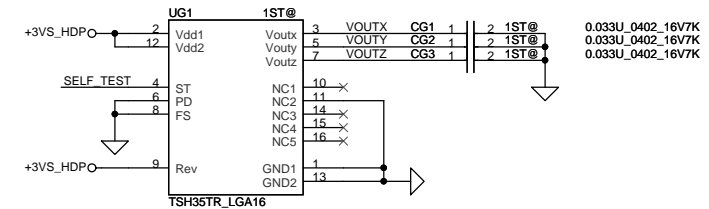
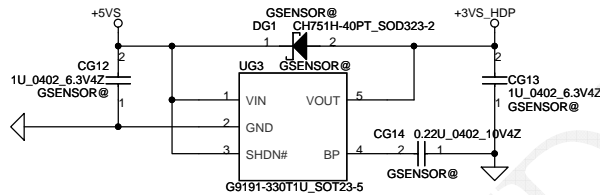
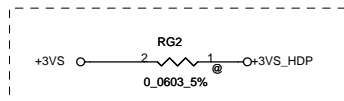


## LPC Debug Port

Please place the PAD under DDR DIMM.



## G-Sensor



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### +3VALW TO +3VS

Vgs=-0V, Id=9A, Rds=18.5mohm

### +5VALW TO +5VS

### +1.5V to +1.5VS

Vgs=10V, Id=14.5A, Rds=6mohm

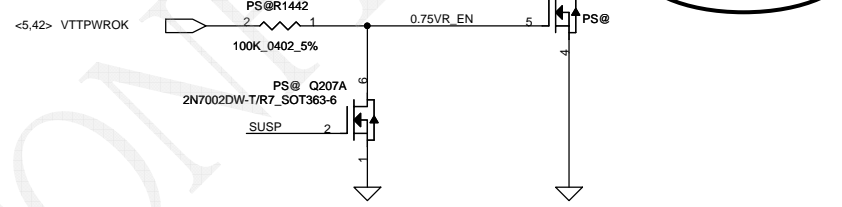
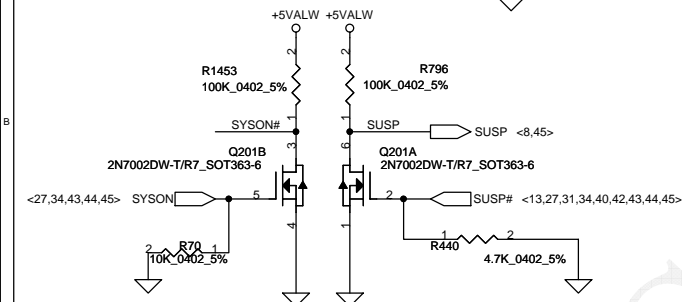
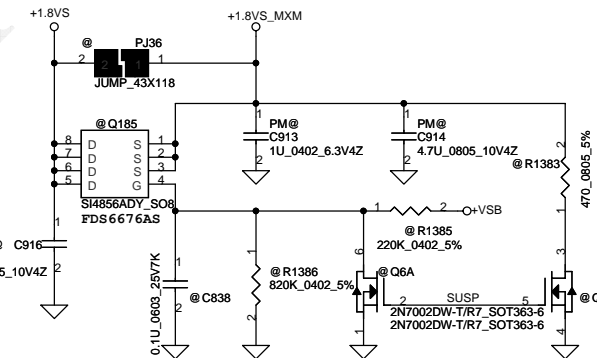
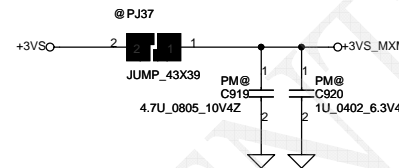
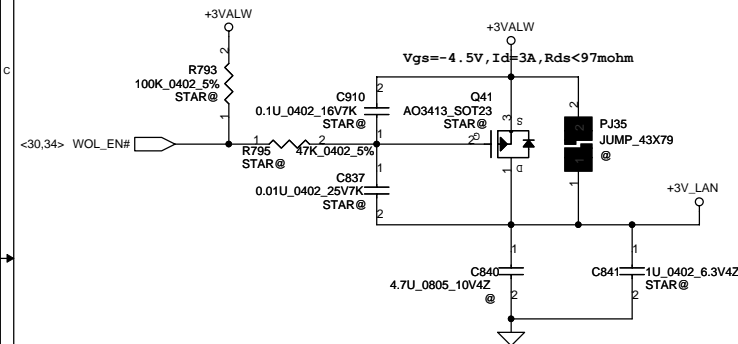
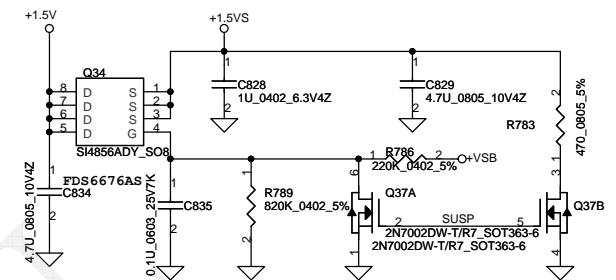
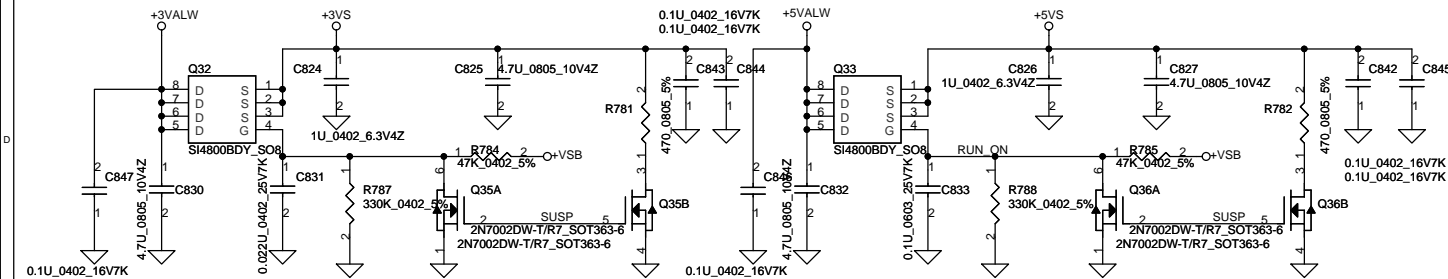
### +3VALW TO +3V\_LAN

### +3VS TO +3VS\_MXM

Vgs=-4.5V, Id=3A, Rds<97mohm

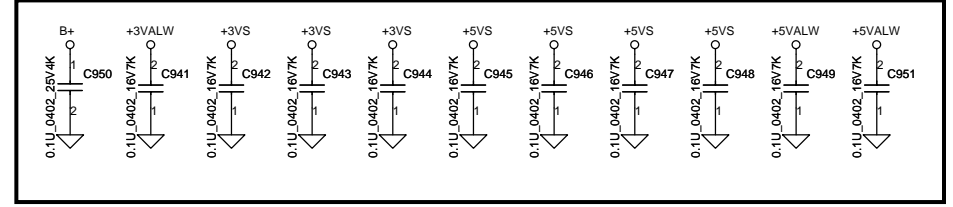
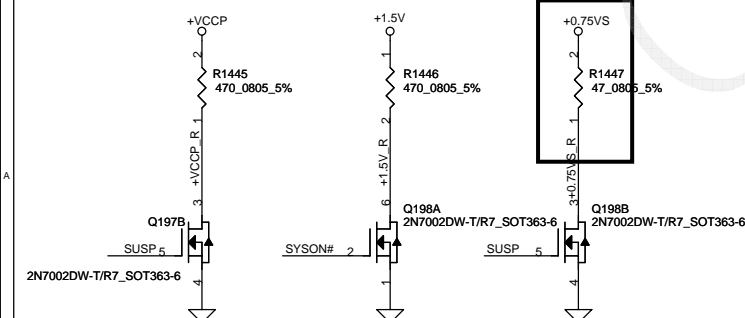
### +1.8V to +1.8VS\_MXM

Vgs=10V, Id=14.5A, Rds=6mohm



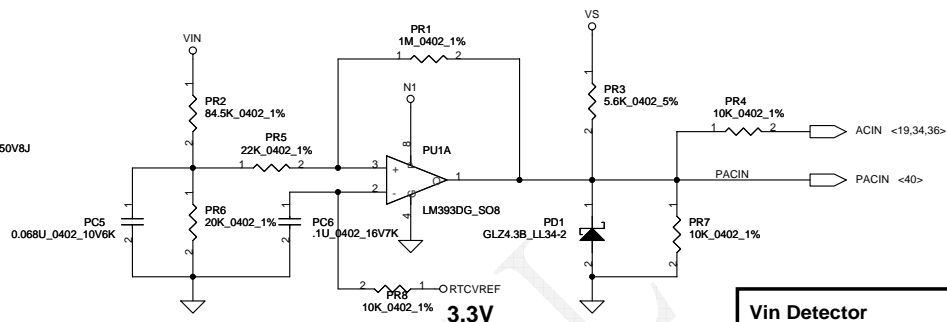
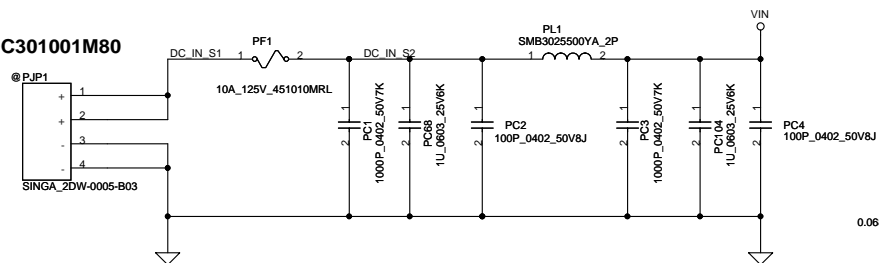
0917 : change from 470 to 47ohm to meet INTEL S3 reduce sequence

09/08 for ESD demand



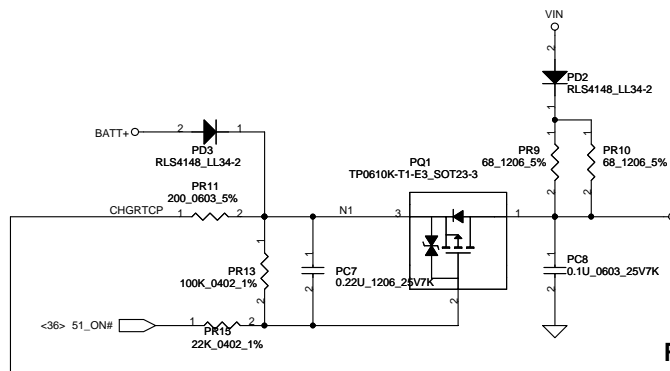
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# DC301001M80

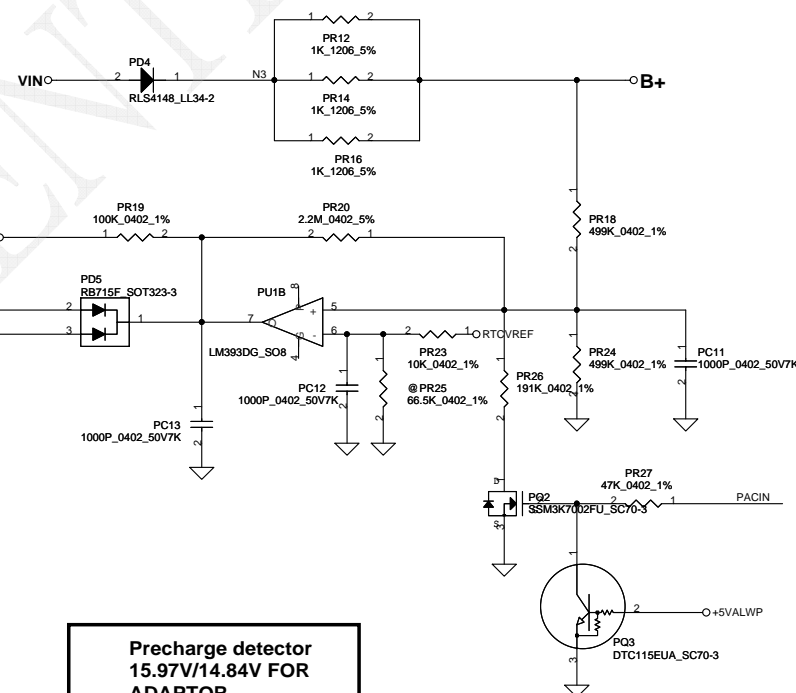
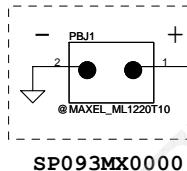


## Vin Detector

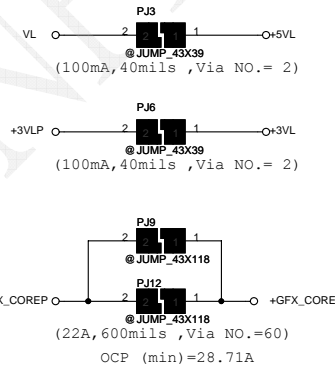
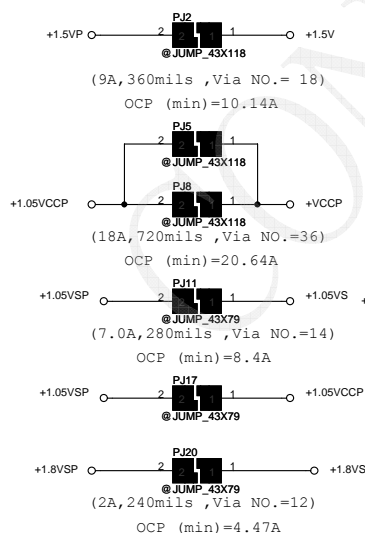
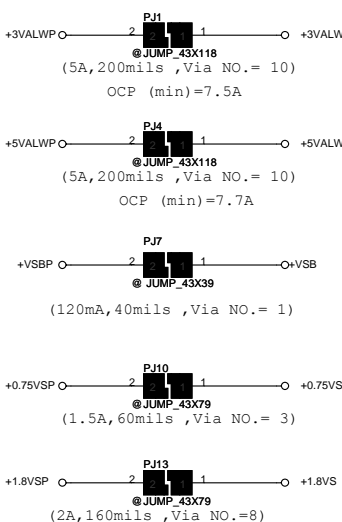
High 18.384 17.901 17.430  
Low 17.728 17.257 16.976



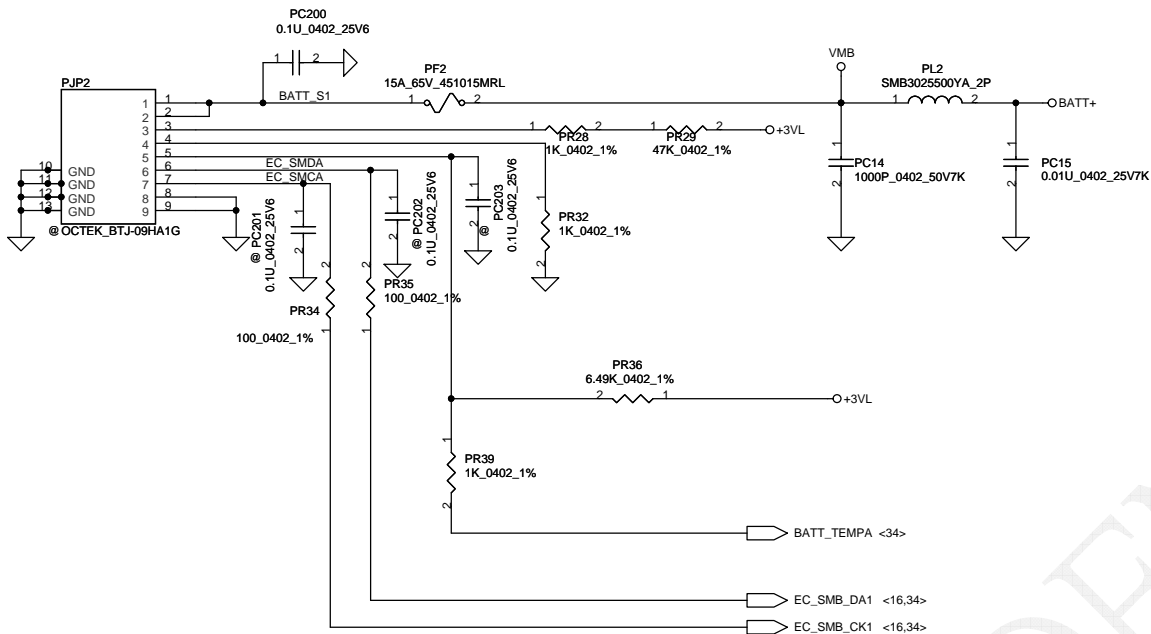
## RTC Battery



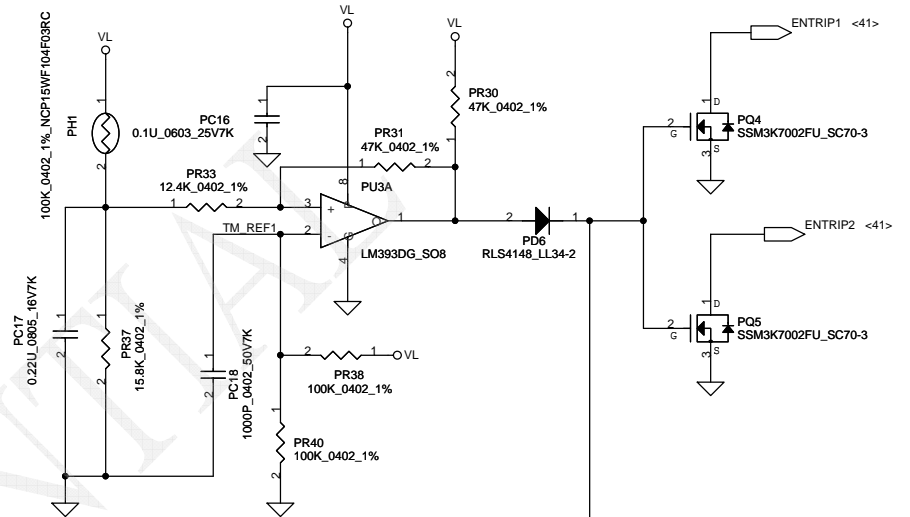
## Precharge detector 15.97V/14.84V FOR ADAPTOR



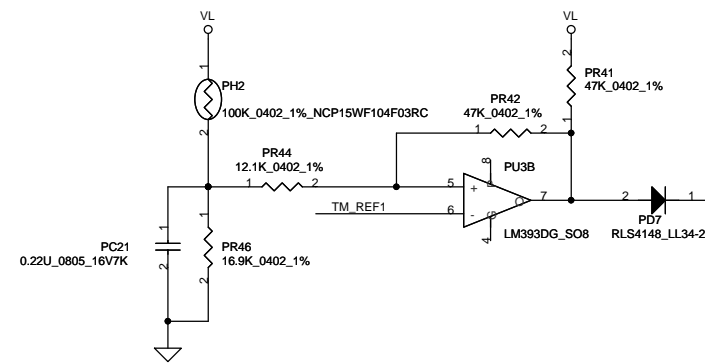
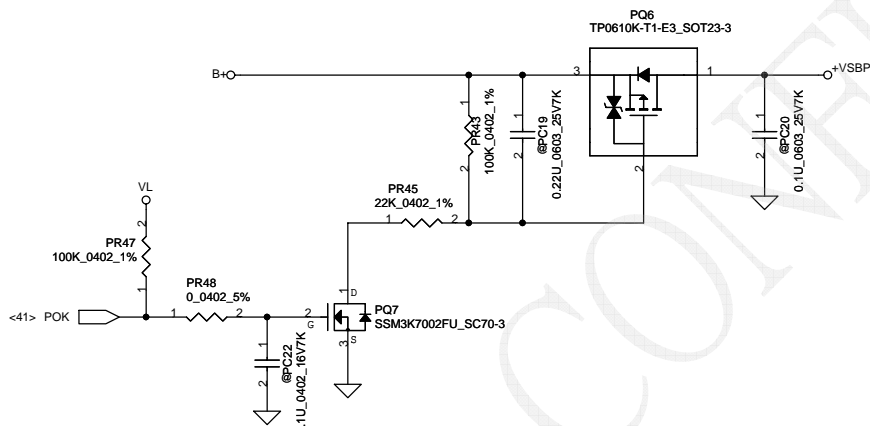
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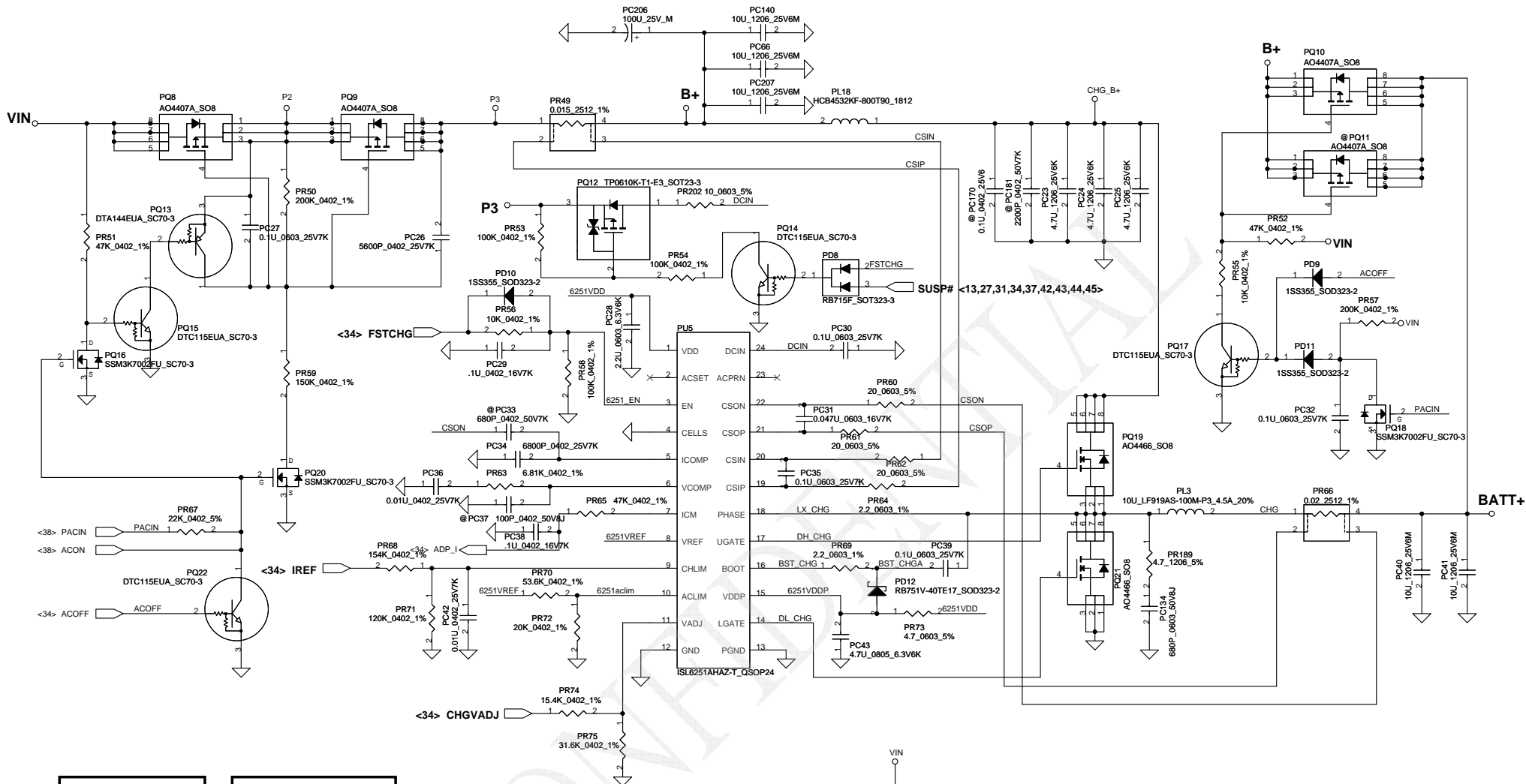
**PH1 under CPU bottom side :**  
 CPU thermal protection at 92 degree C  
 Recovery at 56 degree C



**PH2 near main Battery CONN :**  
 BAT. thermal protection at 90 degree C  
 Recovery at 53 degree C



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CC=0.25A-3.6A

IREF=1.016\*Icharge

IREF=0.228V-3.288V

VCHLIM need over 95mV

CHGVADJ=(Vcell-4)/0.10627

Vcell CHGVADJ

4V 0V

4.2V 1.882V

4.35V 3.2935V

Iada=0~3.947A (75W) CP= 92%\*Iada; CP=3.65A

CP mode

Vacim=2.39\*(20K/(152K/(20K/(152K+24K/(152K))=1.09986V

Iinput=(1/0.02)\*(0.05\*Vacim)/2.39+0.05

where Vacim=1.09986V, Iinput=3.65A

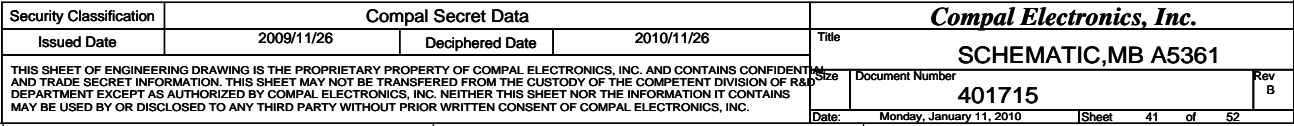
CELLS	VDD	GND	Float
CELL number	4	3	2

90W Iadapter=0~4.74A PR49=0.015 ohm CP=4.357A PR70=53.6K PR72=20K

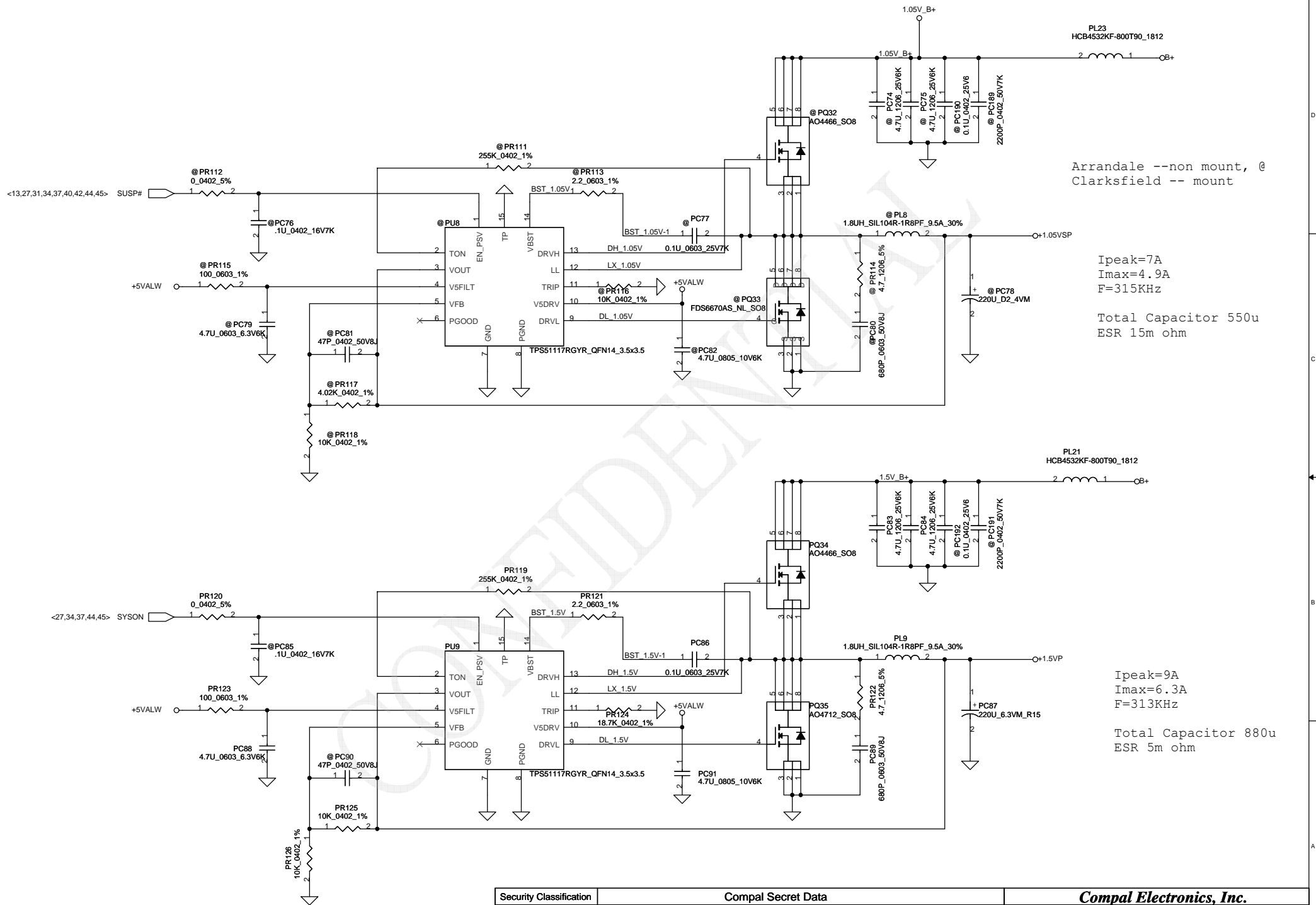
120W Iadapter=0~6.32A PR49=0.015 ohm CP=5.81A PR70=8.25K PR72=26.7K

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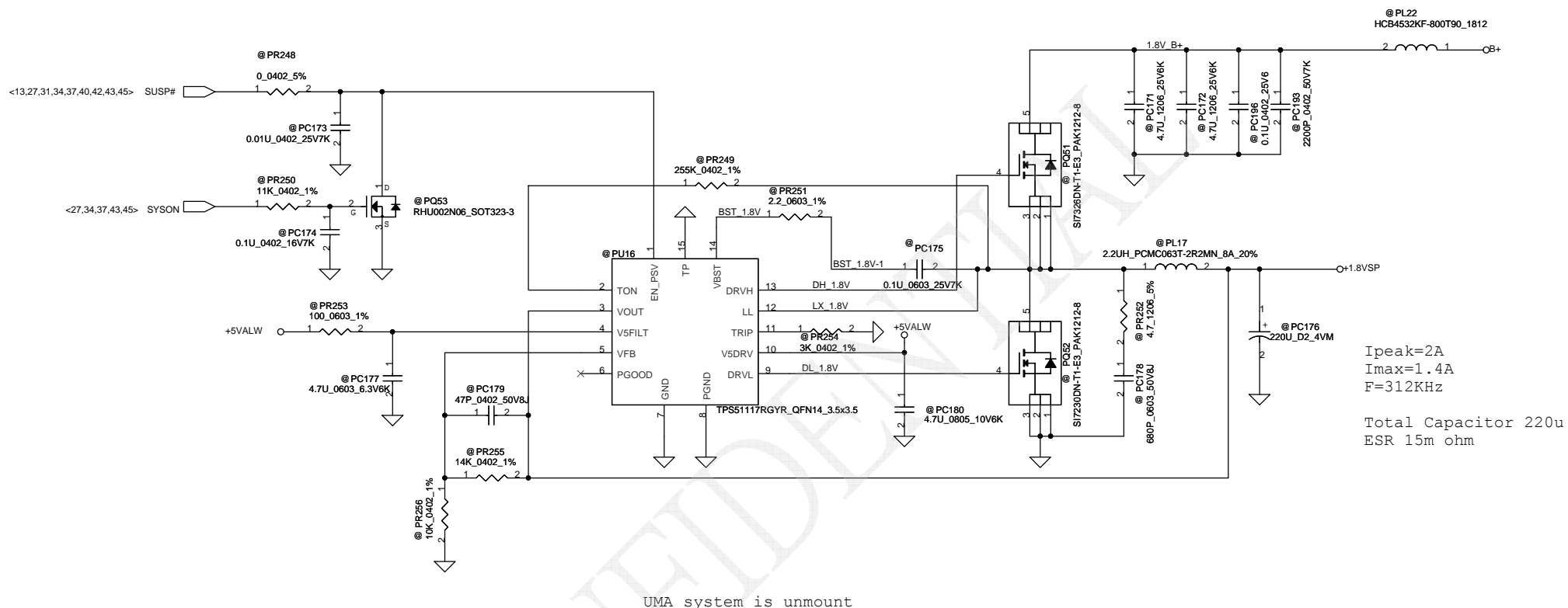




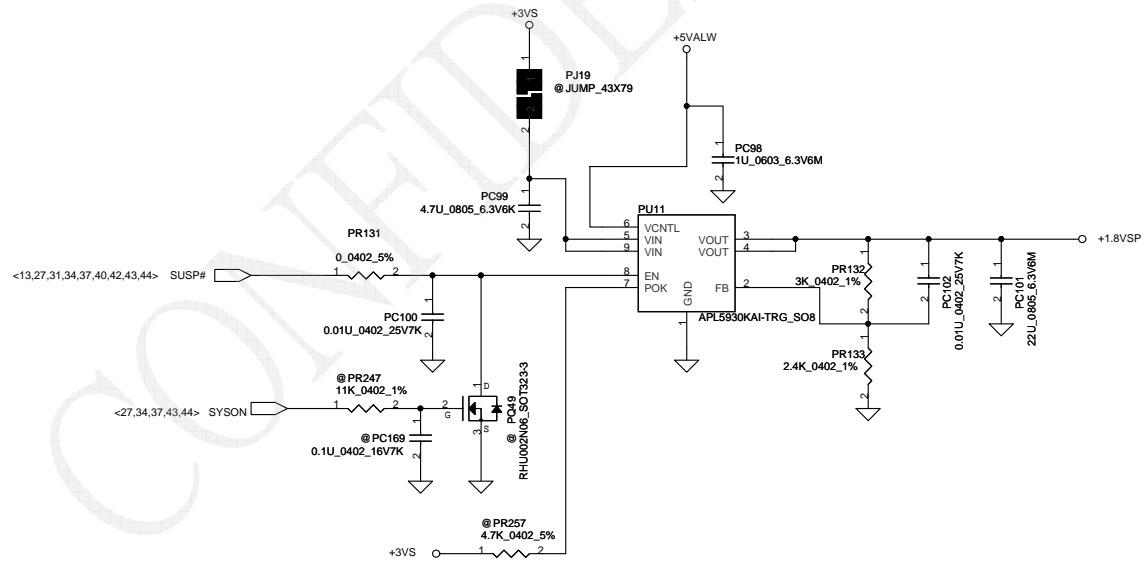
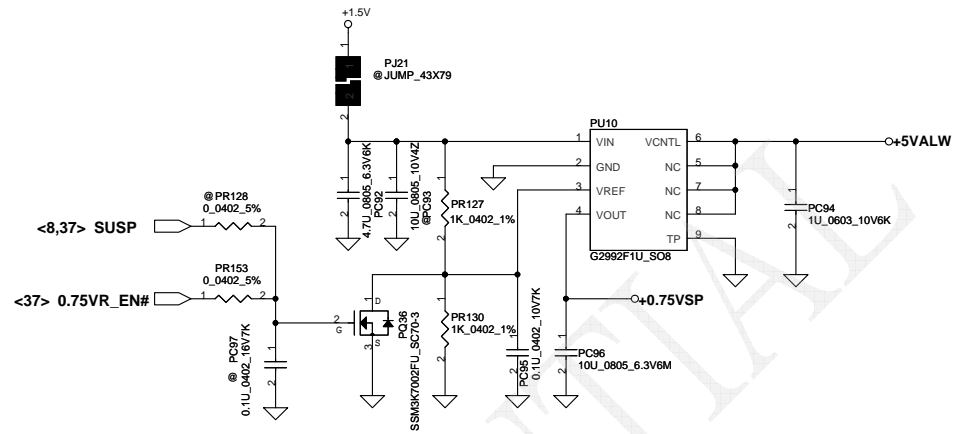




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Discrete system is unmount

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NO DATE	PAGE	MODIFICATION LIST	PURPOSE
EVT	BATTERY CONN/OTP	Change PH1 and PH2 to SL200000V00	Circuit modify
EVT	BATTERY CONN/OTP	Add PC200,PC201,PC202,PC203(0.1uF_0402_1%)	EMI request
EVT	CHARGER	Change PR76 to SD034309380	Circuit modify
EVT	3VALWP/5VALWP	Change PR82 and PR83 to 120k	Circuit modify
EVT	3VALWP/5VALWP	Change P184,PC186,PC197 to SE000006900	Sourcer request
EVT	1.05V_VCCP	Change PL7 to 0.47uH_18A	Circuit modify
EVT	1.05V_VCCP	Change PR95 to 2.2 ohm,PR100 and PC70 to mount	EMI request
EVT	1.05V_VCCP	Change PC199 to SE000006900	Sourcer request
EVT	1.05VSP/1.5VP	Change PR113 to 2.2 ohm,PR114 and PC80 to mount	EMI request
EVT	1.05VSP/1.5VP	Change PR116 to 10KΩ, PR124 to 18.7KΩ	Circuit modify
EVT	CPU_CORE	Change PR141 and PR181 to 2.2 ohm, PR144,PR184,PC108 and PC139 to mount	EMI request
EVT	CPU_CORE	Change PU13 to SA000020U00 ,PL10 to SM010018210 PC141 to SE076104K80	Circuit modify
EVT	CPU_CORE	Change PC198 to SE000006900	Sourcer request
EVT	DCIN/DECTOR	Change PC68,PC104 to SE000006900	Sourcer request
EVT	GM VGA_CORE	Change PC161 to SE071150J80 ,PC141,PC162 to SE076104K80	Circuit modify
EVT2	3VALWP/5VALWP	Change PC45 to 1uF , add PC204 (1uF_0402)	Circuit modify
EVT2	1.05V_VCCP	Change PR94 to 3.4KΩ	Circuit modify
EVT2	1.05VSP/1.5VP	Change PR115 、 PR123 to 100 Ω, PC79、 PC88 to 4.7uF	Circuit modify
EVT2	1.8VSP	Change PR251,PL22 to unmount	Circuit modify
EVT2	0.75VSP/1.8VSP	Change PR132 to 3K Ω, PR133 to 2.4KΩ	Circuit modify
EVT2	CPU_CORE	Change PR150 to 1.91K Ω, PC132、 PC124 to SE124224K80, PC140、 PR195 to unmount, PC107 、 PC131 to 0.22uF delete PR160、 PH3	Circuit modify
EVT2	GM VGA_CORE	Change PC149 to 0.22U_0603_25V,PL15 to 0.56U_25A_20% PR206 to 8.25KΩ ,PR220,PC165 to unmount	Circuit modify
EVT2	1.8VSP	mount PWM circuit	For Discrete system
EVT2	0.75VSP/1.8VSP	mount LDO circuit	For UMA system
PVT	1.05V_VCCP	Change PR99 to 4.99KΩ,PU7 to APW7138,PC71 to unmount PQ55 to SB000009610,PL7 to 1uH_10*10*4 PR94 to 3.4K ohm,PR258 to 1K ohm Delete PC66 Add PC205(0.1uF_0402)	Circuit modify
PVT	1.05V_VCCP	Unmount PQ54,PQ55,PC205,PC228,PC229,PR105,PR290,PR291 ,PR292,PR293,PR294,	For UMA system
PVT	0.75VSP/1.8VSP	Change PQ36 to SB000009610,PC92 to 4.7uF, PU11 to APL5930, signal 0.75VR_EN to 0.75VR_EN#, SUSP# to SUSP,Delete PQ37,PR129	Circuit modify
PVT	GM VGA_CORE	unmount GM VGA_CORE circuit Delete GFXVR_PWRGD signal	For Discrete system
PVT	BATTERY CONN/OTP	Change PR33 to 12.4K ohm,PR37 to 15.8K ohm PR29 pin2,PR36 pin1 connect to+3VL	Circuit modify
PVT	CPU_CORE	Change PC132 to SE076473K80, PL11,PL12,PL13 to SH000000HK00 (DCR 5%)	Circuit modify
PVT	1.05VSP/1.5VP	Change PJ14 to PL23, PR117 to 4.02K ohm, PR118,PR125, PR126 to 10K ohm, PR121,PR122,PC89 to mount	Circuit modify
PVT	CHARGER	ADD PC206,PC66,PC140,PC207(unmount)	EMI request
PVT	CHARGER	Change PR64,PR69 to 2.2_0603_1%, PR189,PR134 to mount	Circuit modify
PVT	CHARGER	Change PR70 to 75K ohm, PQ9 to AO4407A	For UMA system
PVT	1.8VSP	Change PR255 to 14K ohm,PR256 to10K ohm,PR251 to 2.2 ohm	For Discrete system
PVT	3VALWP/5VALWP	Change PR84,PR85 to 2.2_0603_1%, mount PR86,PR87,PC55,PC56	Circuit modify
Pre MP	BATTERY CONN/OTP	Change PC19 to 0.22U_0603_25V	Circuit modify
Pre MP	BATTERY CONN/OTP	Change PR44 to 12.1K ohm	Battery OTP modify
Pre MP	1.05V_VCCP	Change PR94 to 6.81K ohm,PR258 to 2K ohm PR101 to unmount,PR102 to mount	HW request
Pre MP	1.05V_VCCP	Unmount PQ54,PQ55,PC205,PC228,PC229,PR105,PR290,PR291 ,PR292,PR293,PR294,	For Discrete system
Pre MP	0.75VSP/1.8VSP	Change PR128 to unmount,PR153 to mount	HW request
Pre MP	CPU_CORE	Change PR171 to 2.87K ohm,PR188 to 1.27K ohm	CPU_CORE OCP modify
Pre MP	CHARGER	Add PC66,PC140,PC206,PC207	EMI request
Pre MP	1.05VSP/1.5VP	Unmount 1.05VSP circuit	For cost down
Pre MP	1.05VSP/1.5VP	Add PL23	For DIS SKU HW request

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ISPD

PCB

ZZZ  
PCB LA-5361P REV10

DC-IN

PJP1  
PJP1  
45@  
UL3

Trans  
former

T0/100M transformer  
8103EL@

LAN

UL1  
8103EL  
8103EL@

SB\_GL\_R1

U5  
PCH  
HM55R1@

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Revision Change: 0.2 to 0.3

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
1	7/1	36	Mount Q193	LVDS Display
2	7/1	12	Mount : R91, Unmount : R89, C925, C918, L92	Co-layout GEN for low power component
3	7/1	21	U5.BC38 Change UMA_HDMI_TX2+ to UMA_HDMI_TX0+ U5.BD38 Change UMA_HDMI_TX2- to UMA_HDMI_TX0- U5.BD40 Change UMA_HDMI_TX0+ to UMA_HDMI_TX2+ U5.BE40 Change UMA_HDMI_TX0- to UMA_HDMI_TX2-	Modify HDMI Trace
4	7/1	18	Mount Socket U59A , Unmount U59	BIOS Interface Change
5	7/1	21	U61.2 Change DDPD_CTRLCLK to DDPD_CTRLDATA U61.5 Change DDPD_CTRLDATA to DDPD_CTRLCLK	Modify DP Dongle Trace
6	7/1	29	Add CN8	Power Solution
7	7/1	32	Mount (0.01uF) CL34, CL35	EMI Solution
8	7/1	39	Mount (0.1uF) : C847, C842, C843, C844, C845, C846	ESD Solution
9	7/1	21	Remove R1291, R1293	BIOS Interface Change
10	7/1	9	Add (22uF) C189, C193, C210, C211, C258, C222, C260, C259, C262, C261, C264, C263, C268, C265, C269, C267	Modify CPU_CORE Quality
11	7/1	13	Mount : R1427, C485, R1414, R1415, R1389	Display for Discrete
12	7/1	15	mount (Discrete): R1400, R1401, R1396	Display for Discrete
13	7/1	16	Remove : R134, R135, RV37, RV79	Cost Down
14	7/1	28	Change C350 150uF to 330uF	USB Power Stable
15	7/1	36	Mount Q193	LVDS Display
16	7/1	5	Change R32 3K to 750	S3 Power Saving
17	7/1	5	Mount : R7, R416, R879, D6, C111, U141, PQ56 Umount : R189	S3 Power Saving
18	7/1	8	Mount : Q194, C209, R1407, Q206, R1429, C936, R1408	S3 Power Saving
19	7/1	10	Mount : R60, PQ57, PQ58 , Unmount : R61, R62	S3 Power Saving
20	7/1	39	Mount : PQ60, R1442	S3 Power Saving
21	7/1	39	Add : R1403, R1393, Q192, C839, R1406, R1405	ATI / nVidia Common
22	7/1	21	Add (0.1uF) C934,C935	Add AC decoupling for DP
23	7/1	14	mount : R1440, Mount (Discrete) : R1382, R1392	Display for Discrete
24	7/1	14	Remove : Q195, R1439, R1441	LCDS Inverter Control
25	7/1	14	Change R1189 2.2K to 100K	LVDS Control
26	7/1	39	JCPU.AJ1, JCPU.AF1, JCPU.AE7, JCPU.AE4, JCPU.AC1, JCPU.AB7, JCPU.AB4, JCPU.Y1, JCPU.W7, JCPU.W4, JCPU.U1, JCPU.T7, JCPU.T4, JCPU.P1, JCPU.N7, JCPU.N4, JCPU.L1, JCPU.H1 Change +1.5V to +1.5V_CPU	S3 Power Change
26	8/20	8	C35, C36, C39, C40, C923, C932, C933	BOM Control for GM0
27	8/20	22	R239	Move form P27 to P22
28	8/20	27	JBW, C287, C288, C289, C290, C286, R240, R239, C281, C282, C283, C284, C285, C137	Remove all of components for Braidwood
29	8/20	35	Change RC12, RC13, RC14 from 22 ohm to Bead 120 ohm	For EMI request
30	9/1	34	Add C112	For EMI request
31	9/1	34	Add C794, C820	For EMI request
32	9/1	17	Remove R159, R160, R161, R162, C251, C252, C253, C254	Common Function (NSWAA)
33	9/1	14	Add R1190 (100K)	Solve leakage for+3VS
34	9/1	20	Add R1242 (1K)	Reset function
35	9/1	23	R1331 from +3VS to +3VALW	Change Power Plane
36	9/1	23	R1326 replace 10K to 100K	Solve leakage for CIR
36	9/1	23	R1326 replace 10K to 100K	Solve leakage for CIR

NO	DATE	PAGE	MODIFICATION LIST	PURPOSE
PVT		P05-Auburndale (1/5)-JTAG/XDP	R189 from always to @ PQ56 change to P/N: SB501380020 (BSS138_NL_SOT23-3)	for ITEL S3 reduce solution for ITEL S3 reduce solution demand
PVT		P08-Auburndale (4/5)-PWR	Add C937~C938 Change R1408 from 100k to 470ohm	for ITEL S3 reduce solution demand for meet INTEL S3 reduce sequence
PVT		P10-DDRIII-SODIMMO	Add R1479, R1480, R1481, R1482	for INTEL CFD CPU +S3 reduce solution
PVT		P13-MXM connector	Remove DP fuction Remove VGA_SPDIF signal.	GPU don't need SPDIF
PVT		P14-LVDS	Remove R1340 ~ R1355 Change R626 from GM@ to always mount.	for layout routing. for LVDS POWER ENVDD
PVT		P16- HDMI Conn.	Change R1394 from HDMI@ to PM@	for Discrete SKU without HDMI
PVT		P18-IBEX-M(2/9)-PCI-E/SMBUS/CLK	U59 Change to 4MB size	for SW demand
PVT		P19-IBEX-M(3/9)-DMI/GPIO/LVDS	R1248 Change to @ Reserve C953 180pF	for VGATE to avoid noise
PVT		P19-IBEX-M(3/9)-DMI/GPIO/LVDS	R1248 Change to @ Reserve C953 180pF	for VGATE to avoid noise
PVT		P21-IBEX-M(5/9)-PCI/USB/NAND	R1235 Change from IDP@ to @ Add R1475	Common with NSWAA Common with NSWAA
PVT		P22-IBEX-M(6/9)-CPU/GPIO	Add VGA_HDMI_HPD to PCH Change GPIO17 to 3G_off#, del R1282 Change R1261 from 0ohm to 56ohm	Common with NSWAA Add 3G control Signal Intel Spec
PVT		P26-HDD/SSD/ODD/eSATA/USB Conn	Reserve C952	for IMIC to reduce +5VS noise
PVT		P27-NEW CARD/BCAS	Add Choke L93 R1477 R1478 CB1 change from 4.7uF_0603 to 0.1uF_0402 RB5 change from 33k_0402 to 47k_0402 CB2 change from 0.033uF_0402 to 0.01uF_0402	for EMI demand for BCAS ISSUE for BCAS ISSUE for BCAS ISSUE
PVT		P29-PCIE-WLAN/GPS/TV/UWB	Change DM5 power to +3VS Change RM5 pull high to +3VS Change JGPS.20 to 3G_off#, add R1483.	for LAYOUT for LAYOUT for 3G control
PVT		P30-PCIE-RTL8103EL/RTL8111DL	Reserve R1476 Reserve CL45, CL46	for LOW POWER LAN IC: ISOLATE function for EMI ISN LAN test
PVT		P32-AMP/Audio Jack/HP/SPK	UA3 change to connector GND instead of GNDA Remove VGA_SPDIF signal. Remove LA11 Add RA35, RA36	for LAYOUT GPU don't need SPDIF for internal MIC experiment
PVT		P33-JM385 CardReader	Add thermalpad pin and connect to GND for UC1 UC1 P/N change from SA00003IL00 to SA00003G010	LAYOUT Modify for BOM
PVT		P35-ROM/LID/Debug PORT/GSENSOR	U46 Change to 256KB	for EC demand
PVT		P37-DC INTERFACE	Add C941~C951 for ESD demand Change R1447 from 470ohm to 47ohm	for ESD demand for meet INTEL S3 reduce sequence

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MODIFICATION LIST

PURPOSE

PRE-MP	P05-Auburndale (1/5)-JTAG/XDP	Change R189 from @ to always mount. D6 change to to SCS00004000 Add R1488 Reserve C954 for DRAMPWROK	for ITEL S3 reduce solution demand for ITEL S3 reduce solution demand for ITEL S3 reduce solution demand for ITEL S3 reduce solution demand
PRE-MP	P08-Auburndale (4/5)-PWR	Change R49 to SD028330080	for +GFX_CORE Power down glitch
PRE-MP	P10-DDRIII-SODIMM	Reserve C955 for SM_DRAMRST#	for ITEL S3 reduce solution demand
PRE-MP	P14-LVDS	Change R1439 to GM@, R1440 to PM@ Add C958 for LCD_BL_PWM Change R626 to GM@ 100Kohm, PM@ 10Kohm	for Intel DTST function by ESD demand
PRE-MP	P15-CRT	Change L18, L19, L20 to SM01000DS00 footprint:CHILI_NBQ100505T-800Y-N_2P L21, L22 footpront change to R_0402	for Layout for Layout
PRE-MP	P16- HDMI Conn.	Change HDMI Common Choke to 120ohm WCM-2012-121T SM070000N00 Change D54, R571 BOM Structure from HDMI@ to H@ Change R1394 BOM Structure from PM@ to H@ for HMDI HPD. Add R442, R443 to reduce DIS DDC cap value	by EMI demand for HDMI CEC TVAP
PRE-MP	P17-IBEX-M(1/9)-HDA/JTAG/SATA	Remove U59A Change C205, C204 from 18pF to 15pF	for RTC CLK
PRE-MP	P18-IBEX-M(2/9)-PCI-E/SMBUS/CLK	Change UWB_OFF# from EC to PCH GPIO67 Change R1230 from PM@ to @ Change R1216, Y3, C869, C225 to GM@ Set C869 PM@ 0ohm when Dis SKU	DIS SKU DON'T NEED DCI DIS SKU DON'T NEED DCI DIS SKU DON'T NEED DCI
PRE-MP	P19-IBEX-M(3/9)-DMI/GPIO/LVDS	Change UWB_OFF# from EC to PCH GPIO67 Change R1230 from PM@ to @ Change R1216, Y3, C869, C225 to GM@ Set C869 PM@ 0ohm when Dis SKU R1239 change from 1Kohm to 10Kohm.	DIS SKU DON'T NEED DCI DIS SKU DON'T NEED DCI DIS SKU DON'T NEED DCI
PRE-MP	P20-IBEX-M(4/9)-CRT/LVDS/HDMI	Reserve R1490	for UMA HDMI
PRE-MP	P21-IBEX-M(5/9)-PCI/USB/NAND	Change R1269, R1270, R1272, to SD028470A80	for GREEN BOM
PRE-MP	P22-IBEX-M(6/9)-CPU/GPIO	Change TEMP_ALERT# pull high to +3VS Reserve C957 for RST_GATE Change C957 from 1000pF to 47nF (SE076473K80) Change C957 to PS@	for ITEL S3 reduce solution demand for ITEL S3 reduce solution demand for ITEL S3 reduce solution demand
PRE-MP	P29-PCIE-WLAN/GPS/TV/UWB	Change Touch Screen USB to port12.	
PRE-MP	P30-PCIE-RTL8103EL/RTL8111DL	Change UL1 8111DL@ to SA00002UV10 and UL1 8103EL@ to SA00002XC10	for low power LAN IC for low power LAN IC
PRE-MP	P31-HDA-ALC272	Add C960 for AZ_RST_HD#	by ESD demand
PRE-MP	P32-AMP/Audio Jack/HP/SPK	Change UA3 GND to GNDA	
PRE-MP	P33-JM385 CardReader	Add C959 for PLT_RST#	by ESD demand
PRE-MP	P34-LPC-EC-KB926	Change EC.26pin to H_PROCHOT#_EC and add R1489 for H_PROCHOT#	
PRE-MP	P35-ROM/LID/Debug PORT/GSENSOR	Change U34 to SA00003GI00 Change C647 from 10pF to 0.1uF	by ESD demand
PRE-MP	P36-Switchs,Connectors & LED	Q56 change from SB570020020 to SB00000M700 Reserve D88, R444, R445	by ESD demand
PRE-MP	P37-DC INTERFACE	Q198, Q201, Q207 change to SB00000E000 Change R1447 to SD002470A80 for GREEN BOM	

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